

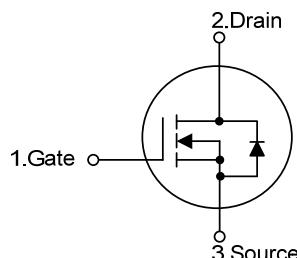
15N60**Power MOSFET****15A, 600V N-CHANNEL
POWER MOSFET****■ DESCRIPTION**

The UTC **15N60** is an N-channel mode power MOSFET using UTC's advanced technology to provide customers with planar stripe and DMOS technology. This technology is specialized in allowing a minimum on-state resistance and superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

The UTC **15N60** is universally applied in active power factor correction and high efficient switched mode power supplies.

■ FEATURES

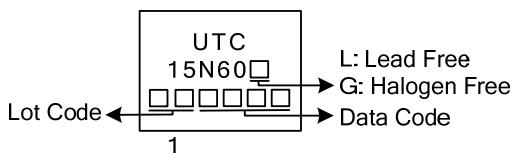
- * $R_{DS(ON)} < 0.65\Omega$ @ $V_{GS}=10V$, $I_D=7.5A$
- * High switching speed
- * Improved dv/dt capability

■ SYMBOL**■ ORDERING INFORMATION**

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
15N60L-TF1-T	15N60G-TF1-T	TO-220	G	D	S	Tube
15N60L-TF1-T	15N60G-TF1-T	TO-220F1	G	D	S	Tube
15N60L-TF2-T	15N60G-TF2-T	TO-220F2	G	D	S	Tube
15N60L-T47-T	15N60G-T47-T	TO-247	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

15N60L -TF1 -T 	(1) Packing Type (2) Package Type (3) Green Package	(1) T: Tube (2) TF1: TO-220F1, TF2: TO-220F2, T47: TO-247 (3) L: Lead Free, G: Halogen Free and Lead Free
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■ MARKING

■ ABSOLUTE MAXIMUM RATINGS ($T_c=25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain to Source Voltage		V_{DSS}	600	V
Gate to Source Voltage		V_{GSS}	± 30	V
Continuous Drain Current	Continuous	I_D	15	A
	Pulsed (Note 2)	I_{DM}	60	A
Avalanche Current (Note 2)		I_{AR}	6.4	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	205	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	2.45	V/ns
Power Dissipation	TO-220	P_D	250	W
	TO-220F1		54	W
	TO-220F2		52	W
	TO-247		312	W
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature.

3. L=10mH, $I_{AS}=6.4\text{A}$, $V_{DD}=50\text{V}$, $R_G=25\Omega$, Starting $T_J=25^\circ\text{C}$

4. $I_{SD} \leq 15\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J=25^\circ\text{C}$

■ THERMAL RESISTANCES CHARACTERISTICS

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220/TO-220F1	θ_{JA}	62.5	$^\circ\text{C/W}$
	TO-220F2		40	$^\circ\text{C/W}$
	TO-247		40	$^\circ\text{C/W}$
Junction to Case	TO-220	θ_{JC}	0.5	$^\circ\text{C/W}$
	TO-220F1		2.3	$^\circ\text{C/W}$
	TO-220F2		2.4	$^\circ\text{C/W}$
	TO-247		0.4	$^\circ\text{C/W}$

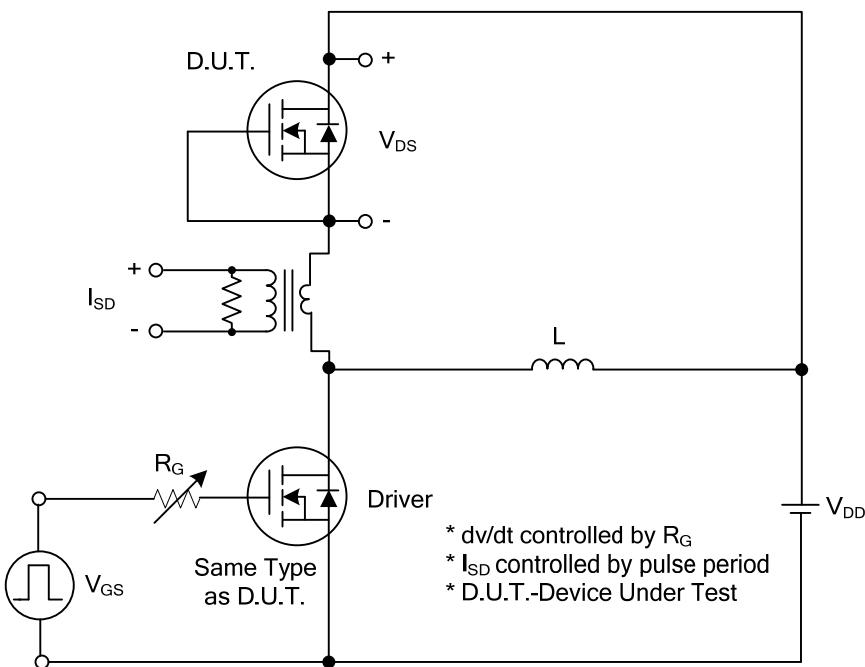
■ ELECTRICAL CHARACTERISTICS ($T_c=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	600			V
Drain-Source Leakage Current	I_{DSS}	$\text{V}_{\text{DS}}=600\text{V}, \text{V}_{\text{GS}}=0\text{V}$		1		μA
Gate- Source Leakage Current	Forward	$\text{V}_{\text{GS}}=+30\text{V}, \text{V}_{\text{DS}}=0\text{V}$		+100		nA
	Reverse	$\text{V}_{\text{GS}}=-30\text{V}, \text{V}_{\text{DS}}=0\text{V}$		-100		nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$\text{V}_{\text{GS}(\text{TH})}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	2.0	4.0		V
Drain-Source On-State Resistance	$\text{R}_{\text{DS}(\text{ON})}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=7.5\text{A}$		0.65		Ω
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$\text{V}_{\text{DS}}=25\text{V}, \text{V}_{\text{GS}}=0\text{V}, \text{f}=1.0\text{MHz}$		2600		pF
Output Capacitance	C_{OSS}			260		pF
Reverse Transfer Capacitance	C_{RSS}			22		pF
SWITCHING PARAMETERS						
Total Gate Charge (Note 1)	Q_G	$\text{V}_{\text{DS}}=50\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{I}_D=1.3\text{A}, \text{I}_G = 100\mu\text{A}$ (Note 1, 2)		155		nC
Gate-Source Charge	Q_{GS}			14		nC
Gate-Drain Charge	Q_{GD}			28		nC
Turn-ON Delay Time (Note 1)	$\text{t}_{\text{D}(\text{ON})}$	$\text{V}_{\text{DD}}=30\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{I}_D = 0.5\text{A}, \text{R}_G=25\Omega$ (Note 1, 2)		105		ns
Turn-ON Rise Time	t_R			115		ns
Turn-OFF Delay Time	$\text{t}_{\text{D}(\text{OFF})}$			600		ns
Turn-OFF Fall Time	t_F			120		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_S			15		A
Maximum Body-Diode Pulsed Current (Note 1)	I_{SM}			60		A
Drain-Source Diode Forward Voltage (Note 1)	V_{SD}	$\text{I}_S=15\text{A}, \text{V}_{\text{GS}}=0\text{V}$		1.4		V
Body Diode Reverse Recovery Time	t_{rr}	$\text{I}_S=15\text{A}, \text{V}_{\text{GS}}=0\text{V}, \frac{d\text{I}_F}{dt}=100\text{A}/\mu\text{s}$ (Note 1)		510		ns
Body Diode Reverse Recovery Charge	Q_{rr}			8.2		μC

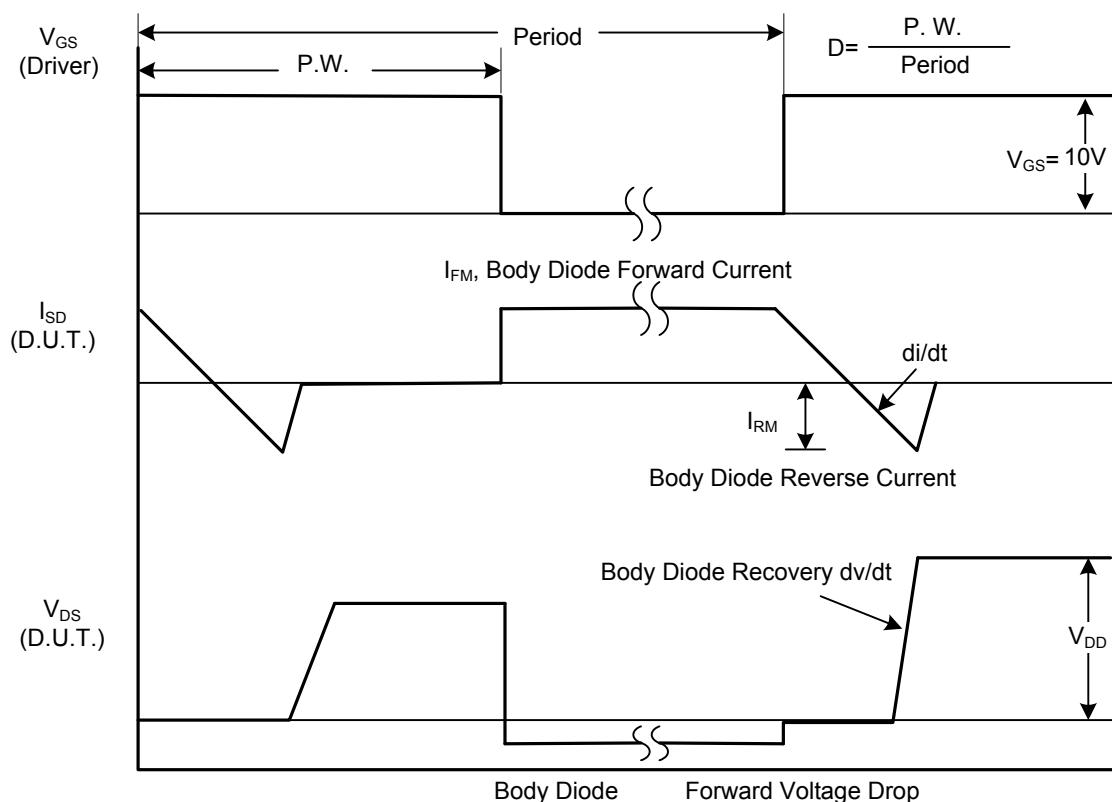
Notes: 1. Pulse Test : Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

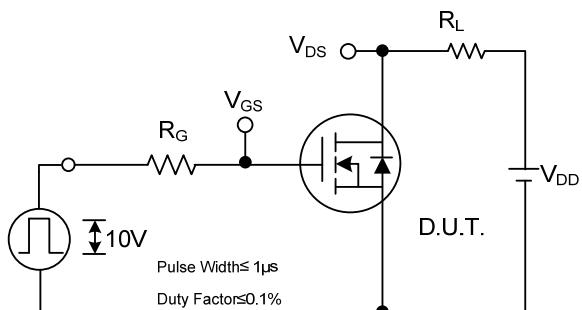


Peak Diode Recovery dv/dt Test Circuit

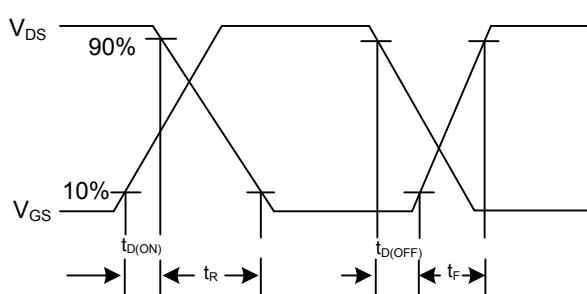


Peak Diode Recovery dv/dt Waveforms

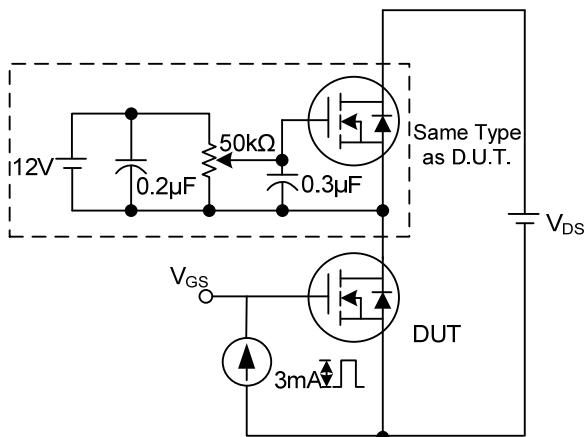
■ TEST CIRCUITS AND WAVEFORMS (Cont.)



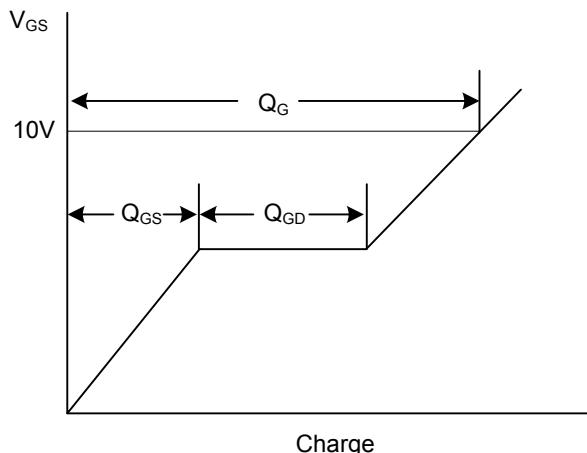
Switching Test Circuit



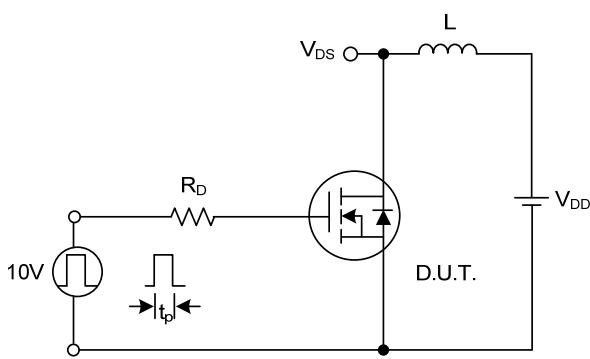
Switching Waveforms



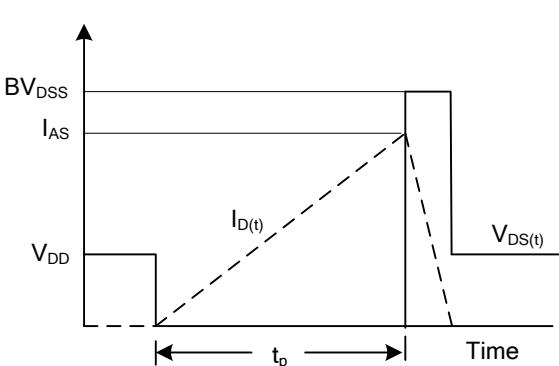
Gate Charge Test Circuit



Gate Charge Waveform

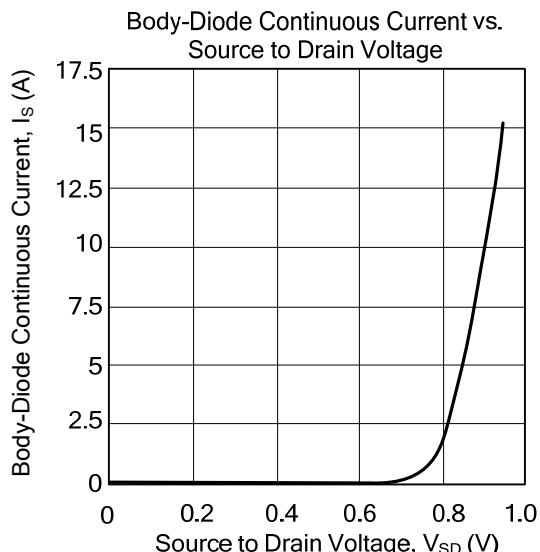
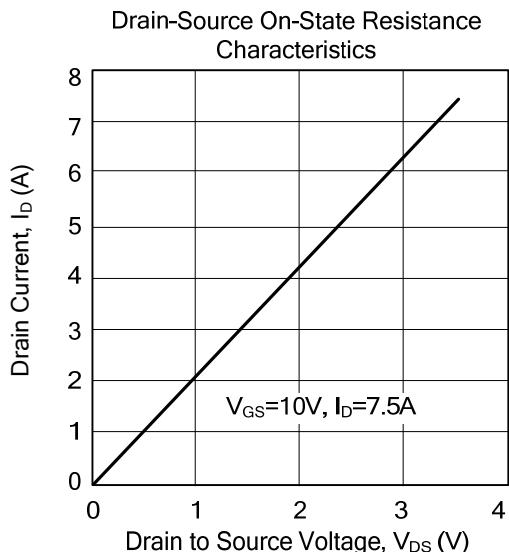
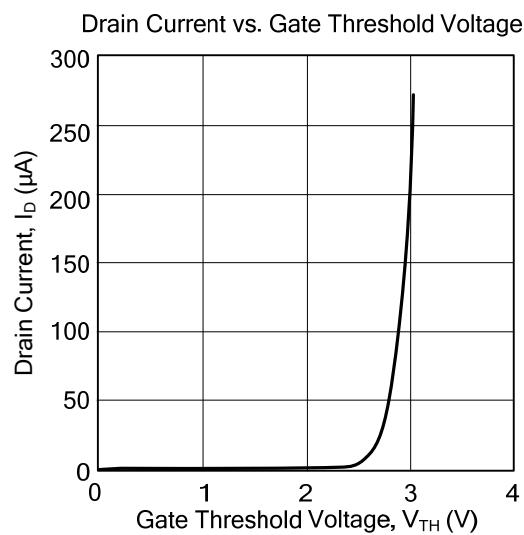
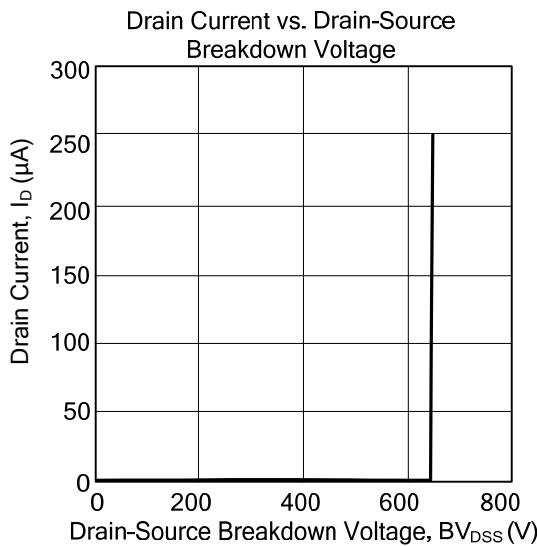


Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

■ TYPICAL CHARACTERISTICS



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