## <u>TOSHIBA</u>

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSIII)

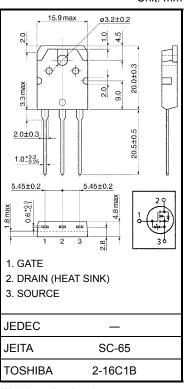
# 2SK2607

Chopper Regulator, DC–DC Converter and Moter Drive Applications

- Low drain-source ON resistance  $: RDS (ON) = 1.0 \Omega (typ.)$
- High forward transfer admittance  $\therefore$  |Y<sub>fs</sub>| = 7.0 S (typ.)
- Low leakage current  $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 640 \ V)$
- Enhancement mode  $: V_{th} = 2.0 \text{ to } 4.0 \text{ V} (V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA})$

Characteris	stics	Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	800	V	
Drain-gate voltage (R	<sub>GS</sub> = 20 kΩ)	V <sub>DGR</sub>	800	V	
Gate-source voltage		V <sub>GSS</sub>	±30	V	
Drain current	DC (Note 1)	I <sub>D</sub>	9	A	
Drain current	Pulse (Note 1)	I <sub>DP</sub>	27		
Drain power dissipation	n (Tc = 25°C)	PD	150	W	
Single pulse avalanche	e energy (Note 2)	E <sub>AS</sub>	778	mJ	
Avalanche current		I <sub>AR</sub>	9	А	
Repetitive avalanche e	nergy (Note 3)	E <sub>AR</sub>	15	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature ra	ange	T <sub>stg</sub>	-55 to 150	°C	

#### Absolute Maximum Ratings (Ta = 25°C)



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristic	Symbol	Max	Unit	
Thermal resistance, channel to case	R <sub>th (ch−c)</sub>	0.883	°C / W	
Thermal resistance, channel to ambient	R <sub>th (ch−a)</sub>	50	°C / W	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 17.4 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 9 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

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Unit: mm

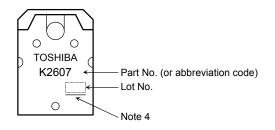
#### **Electrical Characteristics (Ta = 25°C)**

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±30 V, V <sub>DS</sub> = 0 V	_	—	±10	μA
Gate-source bro	eakdown voltage	V (BR) GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 640 V, V <sub>GS</sub> = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	800	-	—	V
Gate threshold v	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	—	4.0	V
Drain-source O	N resistance	R <sub>DS (ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 4 A,	_	1.0	1.2	Ω
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 4 A	3.0	7.0	—	S
Input capacitance	ce	C <sub>iss</sub>			2160	—	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz		45	—	pF
Output capacitance		C <sub>oss</sub>			200	—	
Switching time	Rise time	tr	$V_{GS} \stackrel{10 \text{ V}}{}_{0 \text{ V}} \int I_{D} = 4 \text{ A}$ $V_{GS} \stackrel{10 \text{ V}}{}_{0 \text{ V}} \int I_{D} = 4 \text{ A}$ $R_{L} = 100 \Omega$ $V_{DD} = 400 \text{ V}$	_	25	_	
	Turn-on time	t <sub>on</sub>		_	60	_	ns
	Fall time	t <sub>f</sub>		_	25	_	- 115
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , t <sub>w</sub> = 10 µs	_	110	_	
Total gate charge (gate-source plus gate-drain)		Qg		_	68	_	
Gate-source charge		Q <sub>gs</sub>	V <sub>DD</sub> ≈ 400 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 9 A		38	_	nC
Gate-drain ("miller") Charge		Q <sub>gd</sub>		_	30	_	

#### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	—	_	_	9	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	—	_	_	27	А
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 9 A, V <sub>GS</sub> = 0 V	_	_	-1.9	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 9 A, V <sub>GS</sub> = 0 V, dI <sub>DR</sub> / dt = 100 A / μs		1000		ns
Reverse recovery charge	Q <sub>rr</sub>			12	_	μC

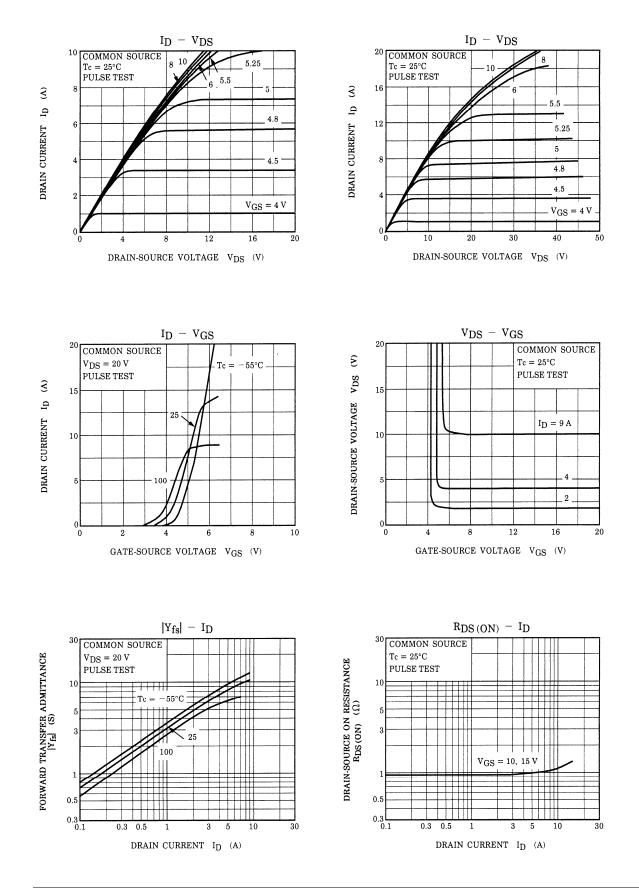
#### Marking



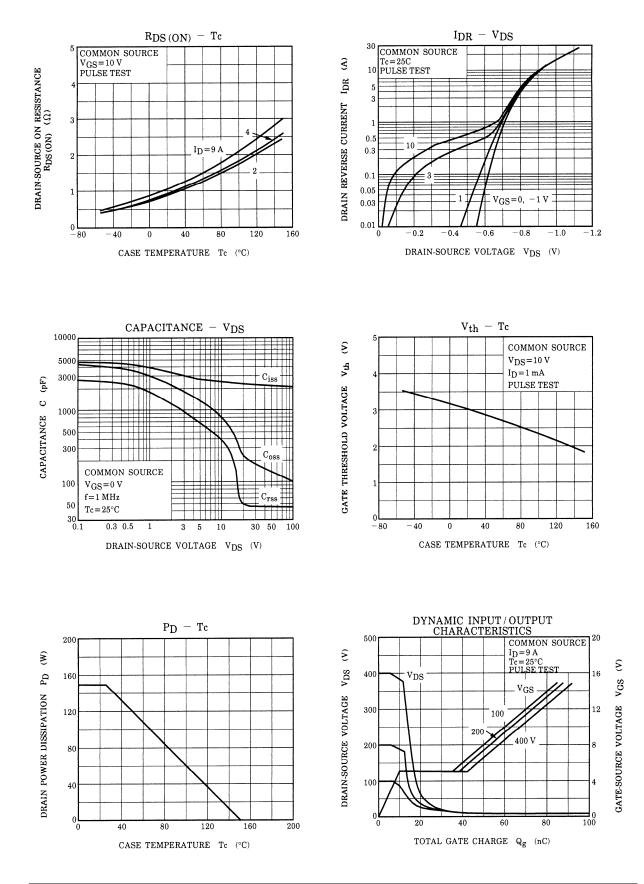
Note 4: A line under a Lot No. identifies the indication of product Labels. Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

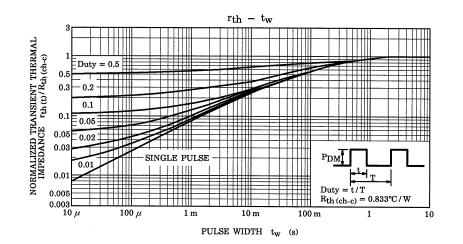
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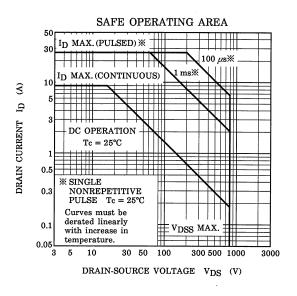
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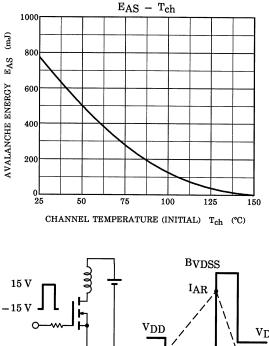


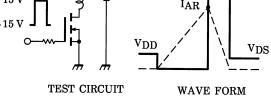
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 $E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left( \frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$  $R_G = 25 \Omega$ VDD = 90 V, L = 17.4 mH

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