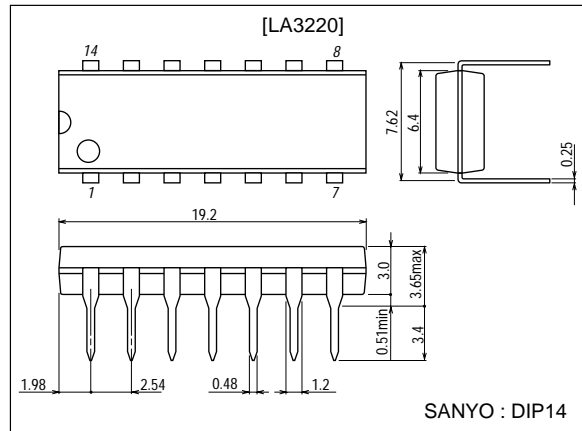


SANYO**LA3220****2-Channel Equalizer Amplifier with ALC****Features**

- Dual pre-amp with built-in ALC (pre-amp $\times 2$ + ALC $\times 2$).
- Due to high gain, recording amp can be formed separately (variable monitor possible).
- ALC and direct motor drive obtained through SEPP output stage.
- Good ALC response balance between channels.
- Good reduced voltage characteristic.
- Excellent channel separation.
- Quick stabilization during power supply input.

Package Dimensions

unit:mm

3003A-DIP14**Specifications****Absolute Maximum Ratings** at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum power supply voltage	V_{CC} max		14	V
Allowable power dissipation	P_d max	$T_a \leq 40^\circ\text{C}$	600	mW
Operating temperature	T_{opr}		-20 to +75	$^\circ\text{C}$
Storage temperature	T_{stg}		-40 to +125	$^\circ\text{C}$
ALC Tr. allowable current			3.5	mA

Operating Conditions at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings	Unit
Recommended power supply voltage	V_{CC}		5 to 13	V
Load resistance	R_L		not less than 680	Ω

Operating Characteristics at $T_a = 25^\circ\text{C}$, $V_{CC}=5\text{V}$, $f=1\text{kHz}$, $R_L=10\text{k}\Omega$ (PB), $R_g=680\Omega$ (REC),

See specified Test Circuit.

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Quiescent current	I_{CCO}			4.5	10	mA
Open voltage gain	V_{GO}			85		dB
Voltage gain	V_G	PB		40		dB
		REC		58		dB
Maximum output voltage	V_O max	THD=1%, PB	0.9	1.2		V

Continued on next page.

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■ SANYO assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all SANYO products described or contained herein.

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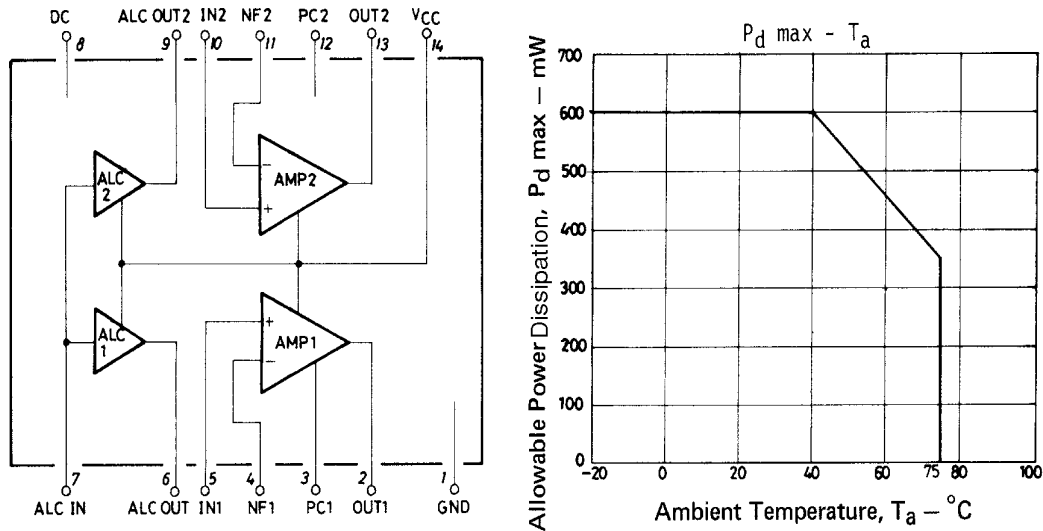
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LA3220

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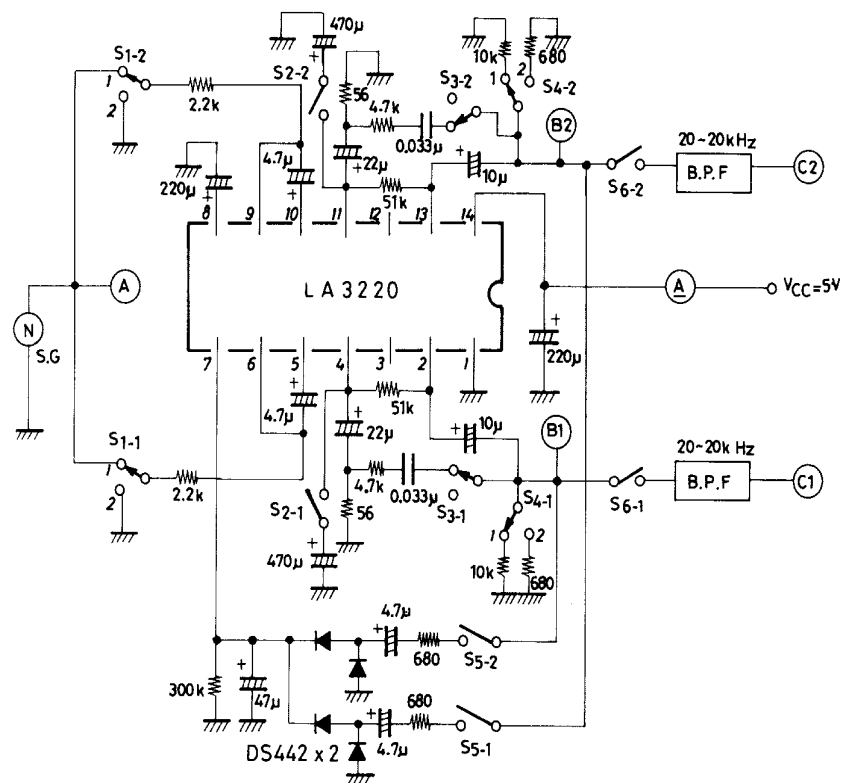
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Total harmonic distortion	THD	$V_O=0.5V$, PB		0.1	1.0	%
Input resistance	r_i		21	30		k Ω
Channel separation	SEP	$R_g=2.2k\Omega$, $V_O=0dB$, PB	40	50		dB
Noise voltage converted to input	V_{NI}	$R_g=2.2k\Omega$, B.P.F.=20Hz to 20kHz, PB		1.0	2.0	μV
ALC width		$V_i=-60dBm$, REC	35	45		dB
ALC balance		$V_i=-20dBm$, REC		0	2.0	dB
ALC distortion		$V_i=-60dBm$, REC		0.5	2.0	%

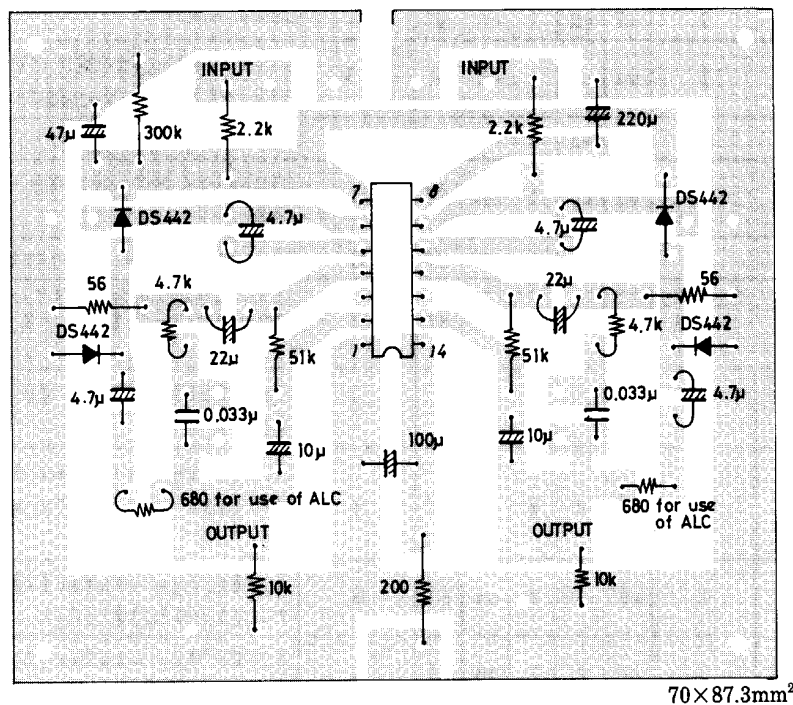
Equivalent Circuit Block Diagram



Test Circuit

Unit (resistance: Ω , capacitance: F)

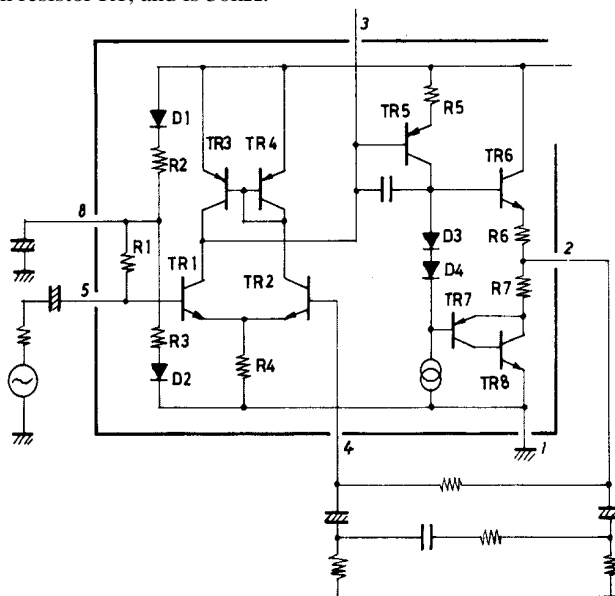




Example of Print Pattern (copper foil side)

1. Circuit Construction

- 1) This is a dual pre-amp composed of $AMP \times 2$, $ACL \times 2$. Input is obtained from NPN differential TR1, TR2 ; and differential load uses active element TR3 to obtain high voltage gain. The output stage is push-pull system with drive for low load impedance, and can be directly connected to ALC circuit and meter circuit. Also, because the amp open loop gain is sufficiently high, it can be used for recording amp and variable monitor is possible. Input impedance is determined by built-in resistor R1, and is $30k\Omega$.



2) ALC Circuit

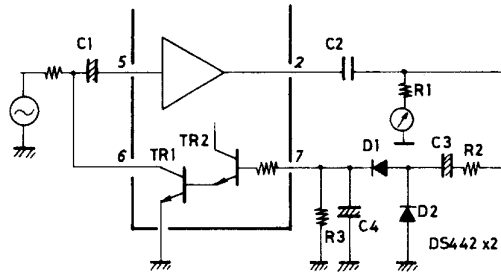
The ALC circuit is composed of TR1, TR2, and due to DC voltage applied to the 7 control terminals, allows variable impedance between TR1 collector and emitter and controls pre-amp input level.

* Attack Time and Recovery Time

Attack time is between when input signal is applied until ALC begins to operate. Recover time is between just by R2, C3 time constant. Recovery time is between when input signal disappears to when amp level returns to the original level. Attack time can be adjusted by C4, R3 time constant.

continued on next page.

The rectification circuit, which obtains ALC control voltage, should be a voltage doubler with superior compression ratio. Also, for low voltage 6V sets, etc., a germanium diode is recommended for D1, D2.



Unit (resistance: Ω , capacitance: F)

2. Closed loop gain VG (f=1kHz)

Closed loop voltage gain is gotten at (f=1kHz) $VG \approx 20\log Z1/Z3$

If $Z1=7.2k\Omega$

$Z3=56\Omega$

$VG=20\log 7.2 \times 10^3 / 56$ becomes=42dB.

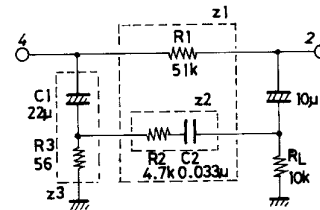
Therefore, equalizer response is determined by these constants.

So, playback amp gain is :

- $20\log R1/R3$ in low frequency regions
- $20\log Z2/Z3$ in high frequency regions

Recording amp gain is

$VG=20\log R1/R3$



3. External constants

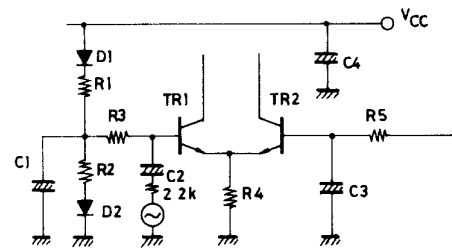
External constants are related to the operation starting time of the amp.

The operation starting time is designed to be within 0.2s, but in this case it is necessary that the differential TR1 and TR2 is the same in time constant. The condition is :

$C1 (R1/R2)=R5 \cdot C3$

Example : If $C1=220\mu$, $R1/R2=5k\Omega$, $R5=51k\Omega$

then $C3$ is 22μ F



- $C1$ is a decoupling capacitor, and its capacity changes the ripple rejection rate. (If capacity is large, ripple rejection rate is large.) It is also related to the amp operation starting time, and when $R5$, $C3$ time constant is large, $C1$ must also be made large. The recommended value is 220μ F.
- $C2$ is an input capacitor, and more than 4.7μ F is recommended.
- $C3$ is an NF capacitor, and determines the low region cut-off frequency. If $C3$ is increased, operation starting time lengthens. 10μ F is recommended. The recommended constants therefore are :

R5 (RF)	C1 (CD)	C2 (IN)	C3 (NF)
51k to 100k Ω	220 μ F	4.7 μ F	22 μ F
200k Ω	330 μ F	10 μ F	10 μ F

We do not recommended more than 200k Ω for $R5$ which lengthens amp operation starting time.

4. Notes on Use

1) Oscillation

When the amp closed loop gain is lowered, oscillation will occur, so when using it with under 40dB gain, connect 10pF between pin 3 and pin 2, and 0.033 μ F (mylar) + 10 Ω to the load end. When closed loop gain is below $VG=30$ dB, it should not be used.

2) Radio Interference Prevention

Connect about 1000pF between input pin (pin 5) and the ground.

3) Maximu Rating

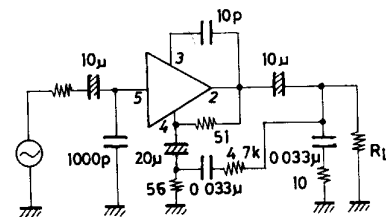
V_{CC} max is $V_{CC}=14$ V, and it should not go over this. The recommended power supply voltage is 5V to 13V.

4) Load Impedance

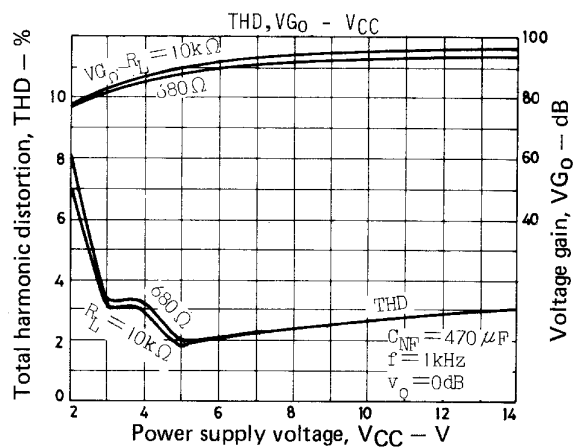
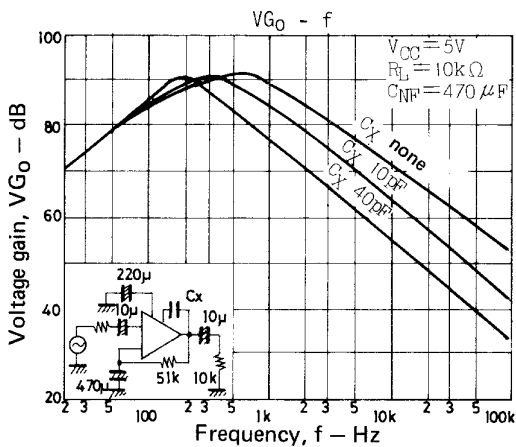
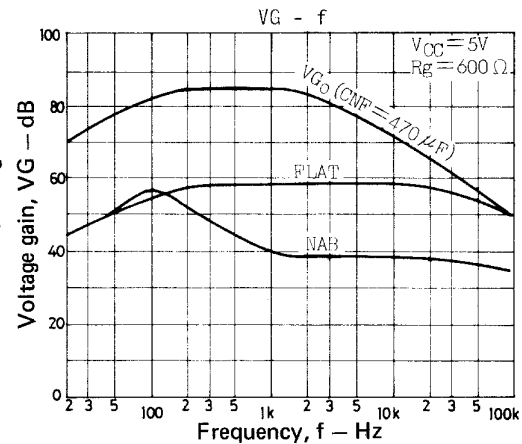
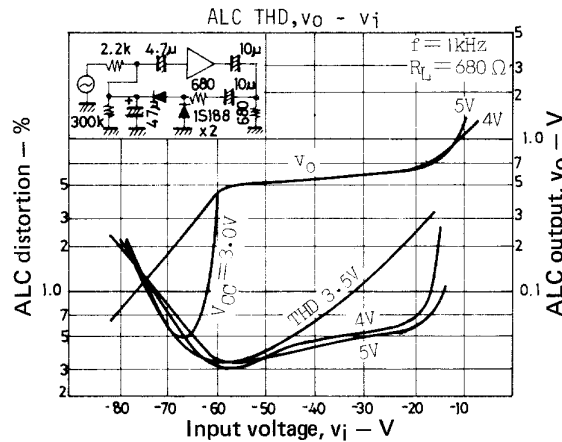
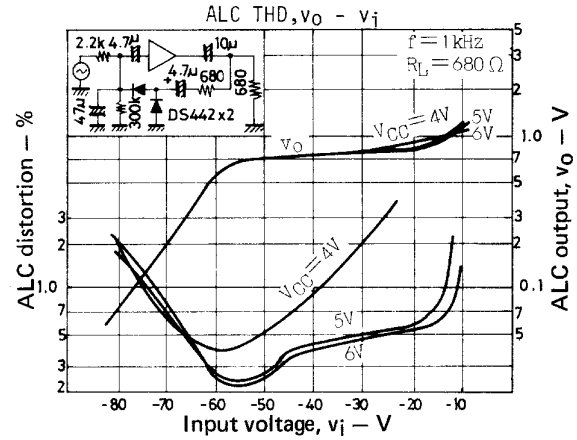
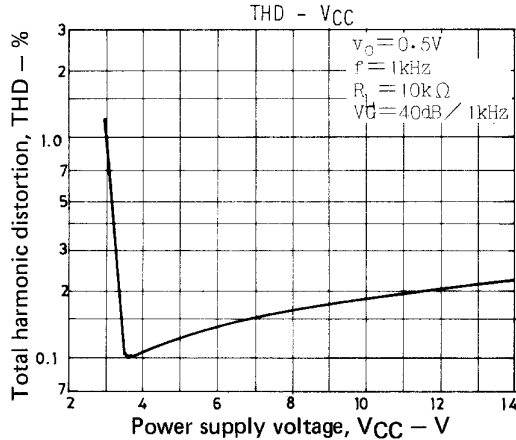
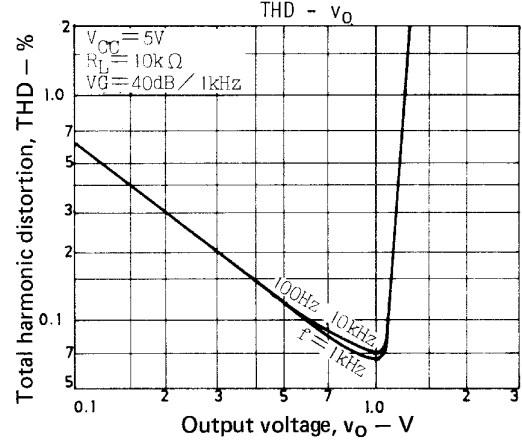
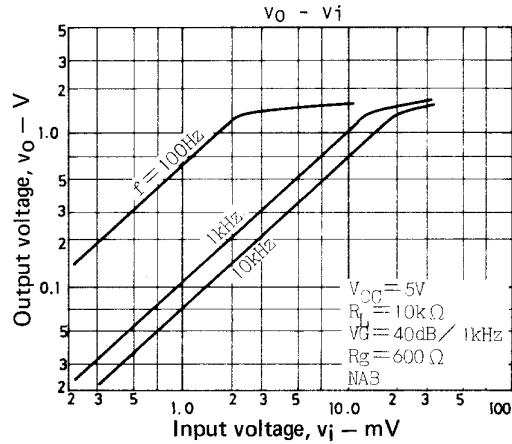
The total load impedance as seen from the output terminal should not be less than 680 Ω .

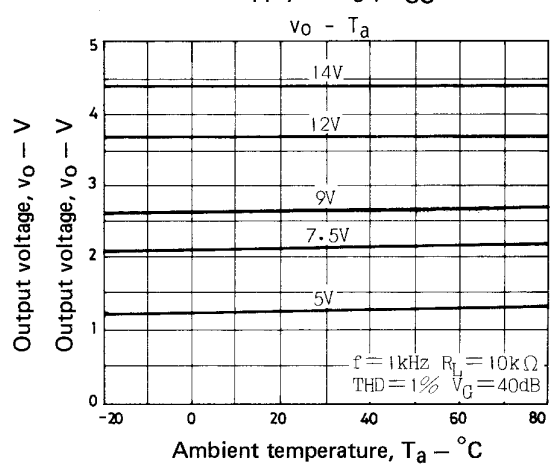
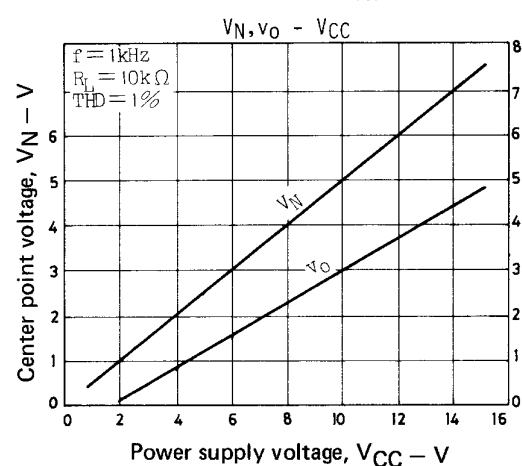
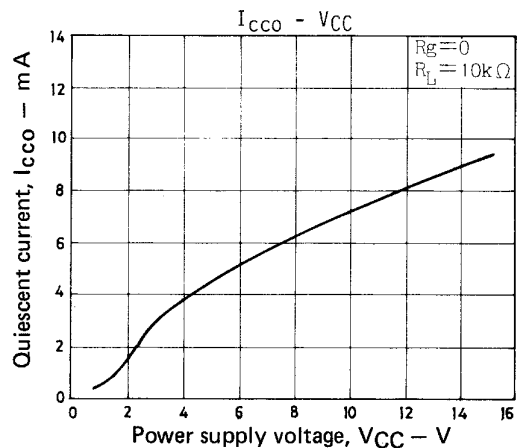
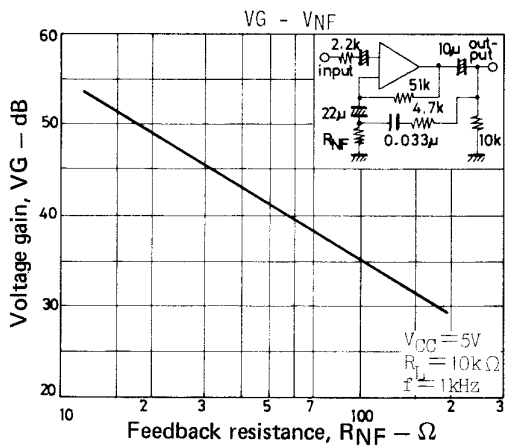
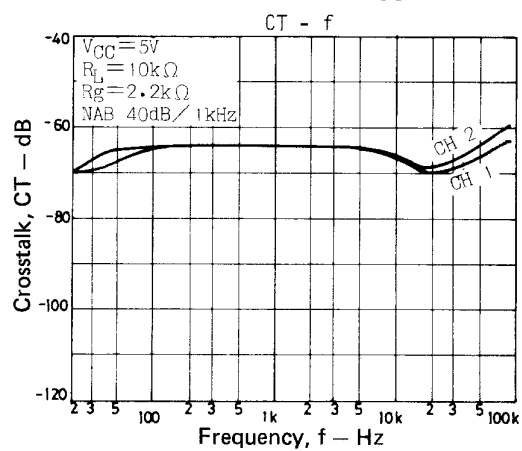
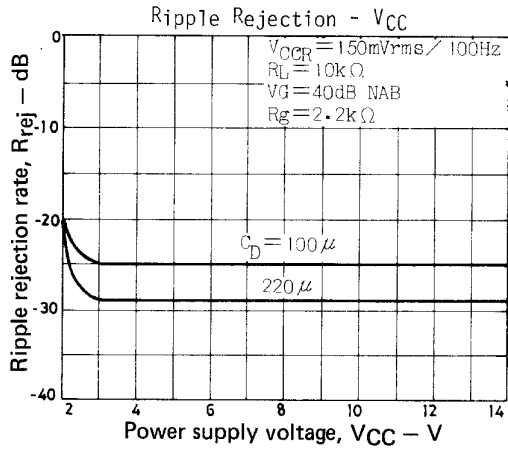
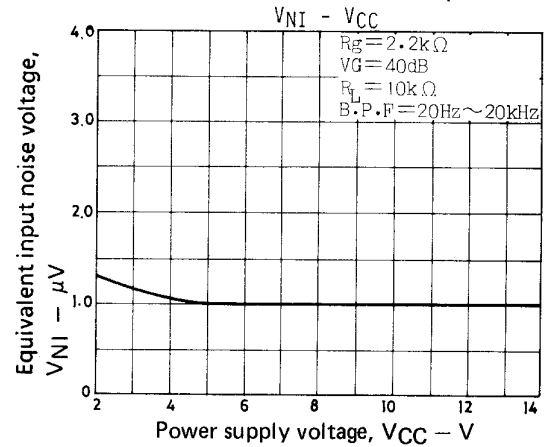
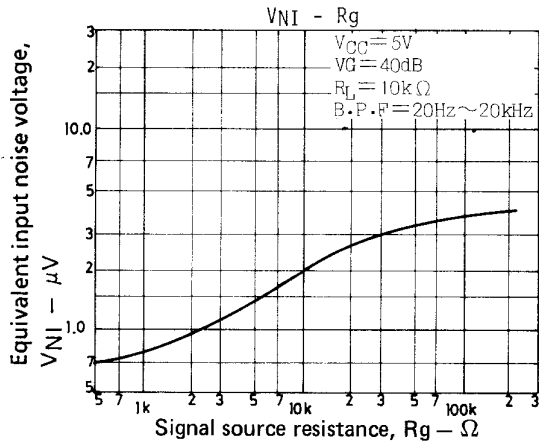
5) A shrt between pins will cause breakdown or deterioration.

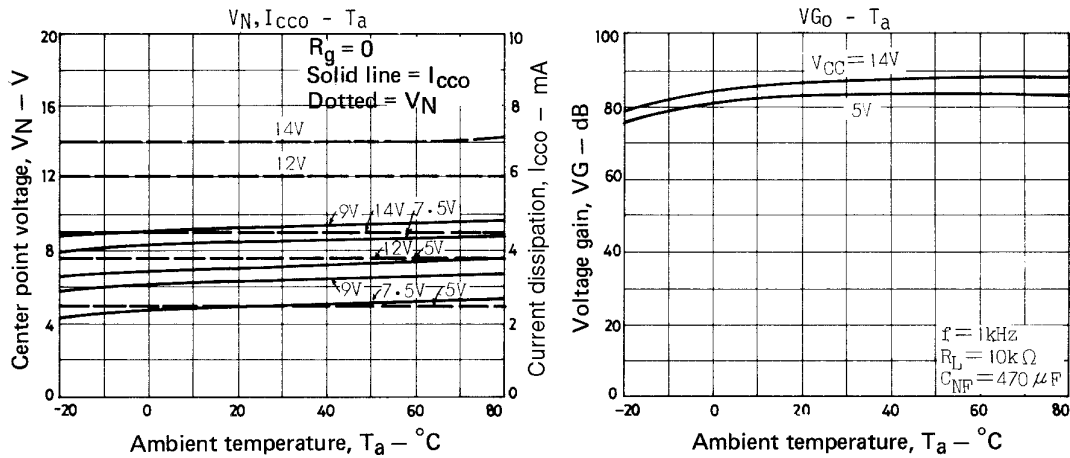
6) A load short will cause breakdown or deterioration.



Unit (resistance: Ω , capacitance: F)

Unit (resistance: Ω , capacitance: F)

Unit (resistance: Ω , capacitance: F)



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