## **TOSHIBA**

TOSHIBA TRANSISTOR SILICON NPN TRIPLE DIFFUSED MESA TYPE

# 2SC5856

HORIZONTAL DEFLECTION OUTPUT FOR SUPER HIGH RESOLUTION DISPLAY, COLOR TV, DIGITAL TV HIGH SPEED SWITCHING APPLICATIONS

- High Voltage : VCBO = 1500 V
- Low Saturation Voltage : V<sub>CE</sub> (sat) = 3 V (max)
- High Speed :  $t_{f(2)} = 0.1 \ \mu s \ (typ.)$

CHARACTERISTIC		SYMBOL	RATING	UNIT		
Collector-Base Voltage		V <sub>CBO</sub>	1500	V		
Collector-Emitter Voltage		V <sub>CEO</sub>	700	V		
Emitter-Base Voltage		V <sub>EBO</sub>	5	V		
Collector Current	DC	Ι <sub>C</sub>	14	A		
	Pulse	I <sub>CP</sub>	28			
Base Current		Ι <sub>Β</sub>	7	А		
Collector Power Dissipation		P <sub>C</sub>	55	W		
Junction Temperature		Tj	150	°C		
Storage Temperature Range		T <sub>stg</sub>	-55~150	°C		

#### ABSOLUTE MAXIMUM RATINGS (Tc = 25°C)



Weight: 5.5 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

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### ELECTRICAL CHARACTERISTICS (Tc = 25°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	Min	Тур.	Max	UNIT
Collector Cut-off Current		I <sub>CBO</sub>	V <sub>CB</sub> = 1500 V, I <sub>E</sub> = 0	_	_	1	mA
Emitter Cut-off Current		I <sub>EBO</sub>	V <sub>EB</sub> = 5 V, I <sub>C</sub> = 0	_	—	100	μA
Collector – Emitter Breakdown Voltage		V (BR) CEO	I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0	700	_	—	V
DC Current Gain		h <sub>FE (1)</sub>	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 2 A	20	_	50	
		h <sub>FE (2)</sub>	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 7.5 A	6.5	_	12.5	
		h <sub>FE (3)</sub>	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 11 A	4.5	_	7.8	
Collector-Emitter Saturation Voltage		V <sub>CE (sat)</sub>	I <sub>C</sub> = 11 A, I <sub>B</sub> = 2.75 A	_	_	3	V
Base-Emitter Saturation Voltage		V <sub>BE (sat)</sub>	I <sub>C</sub> = 11 A, I <sub>B</sub> = 2.75 A	_	1.0	1.4	V
Transition Frequency		f <sub>T</sub>	V <sub>CE</sub> = 10 V, I <sub>C</sub> = 0.1 A	_	2	-	MHz
Collector Output Capacitance		C <sub>ob</sub>	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 1 MHz	_	180	_	pF
Switching Time	Storage Time	t <sub>stg(1)</sub>	I <sub>CP</sub> = 7.5 A , I <sub>B1</sub> (end) = 1.0 A	_	3.5	_	μs
	Fall Time	t <sub>f(1)</sub>	$f_{\rm H} = 32 \text{ kHz}$	_	0.25	_	
	Storage Time	t <sub>stg(2)</sub>	I <sub>CP</sub> = 6.5 A, I <sub>B1</sub> (end) = 0.9 A f <sub>H</sub> = 100 kHz	-	1.8	—	μs
	Fall Time	t <sub>f(2)</sub>		_	0.1	—	







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 $V_{CE (sat)} - I_C$ 



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Reverse Bias – Safe Operating Area

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