

TT2062

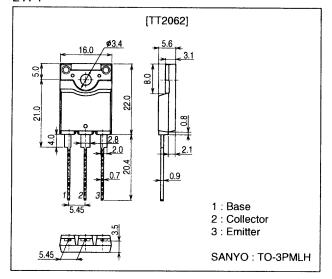
Ultrahigh-Definition CRT Display Horizontal Deflection Output Applications

Features

- · High speed.
- · High breakdown voltage (VCBO=1500V).
- · High reliability (Adoption of HVP process).
- · Adoption of MBIT process.

Package Dimensions

unit: mm 2174



Specifications

Absolute Maximum Ratings at Ta=25°C

| Parameter | Symbol | Conditions | Ratings | Unit |
|------------------------------|------------------|------------|-------------|------|
| Collector-to-Base Voltage | V _{CBO} | | 1500 | V |
| Collector-to-Emitter Voltage | VCEO | | 800 | V |
| Emitter-to-Base Voltage | VEBO | | 5 | V |
| Collector Current | lC | | 18 | Α |
| Collector Current (Pulse) | ICP | | 35 | Α |
| Collector Dissipation | PC | | 3.0 | W |
| | | Tc=25°C | 85 | W |
| Junction Temperature | Tj | | 150 | °C |
| Storage Temperature | Tstg | | -55 to +150 | °C |

Electrical Characteristics at Ta=25°C

| Parameter | Symbol | Conditions | | Ratings | | |
|---------------------------|-----------|--|-----|---------|-----|------|
| | Symbol | | min | typ | max | Unit |
| Collector Cutoff Current | СВО | V _{CB} =800V, I _E =0 | | | 10 | μА |
| Collector Cutoff Current | lCES | V _{CE} =1500V, R _{BE} =0 | | | 1.0 | mA |
| Collector Sustain Voltage | VCEO(sus) | IC=100mA, I _B =0 | 800 | | | ٧ |
| Emitter Cutoff Current | IEBO | V _{EB} =4V, I _C =0 | | | 1.0 | mA |

Continued on next page.

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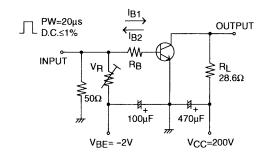
| Parameter | Symbol | Conditions | | Ratings | | |
|---|-----------------------|---|-----|---------|-----|------|
| | | | min | typ | max | Unit |
| Collector-to-Emitter Saturation Voltage | V _{CE} (sat) | IC=10.8A, IB=2.7A | | | 3 | ٧ |
| Base-to-Emitter Saturation Voltage | V _{BE} (sat) | IC=10.8A, IB=2.7A | | | 1.5 | ٧ |
| DC Current Gain | hFE1 | V _{CE} =5V, I _C =1.0A | 15 | | | |
| | hFE2 | VCE=5V, IC=12A | 4 | | 7 | |
| Storage Time | tstg | IC=7A, IB1=1.4A, IB2=-2.8A | | | 3.0 | μs |
| Fall Time | tf | I _C =7A, I _{B1} =1.4A, I _{B2} =-2.8A | | | 0.2 | μs |

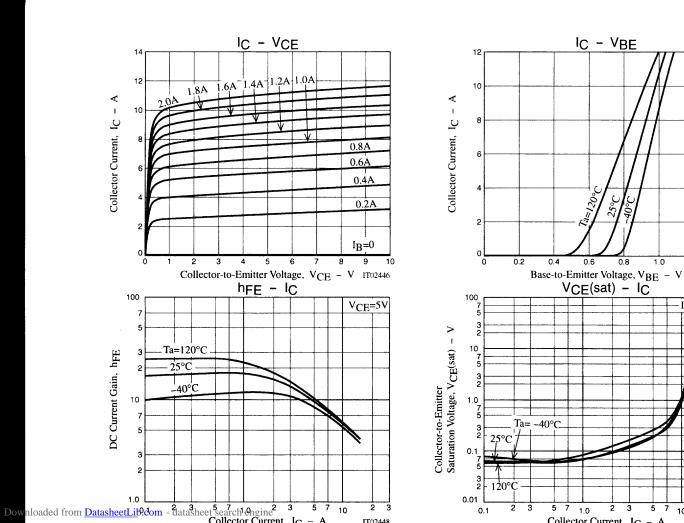
 $V_{CE}=5V$

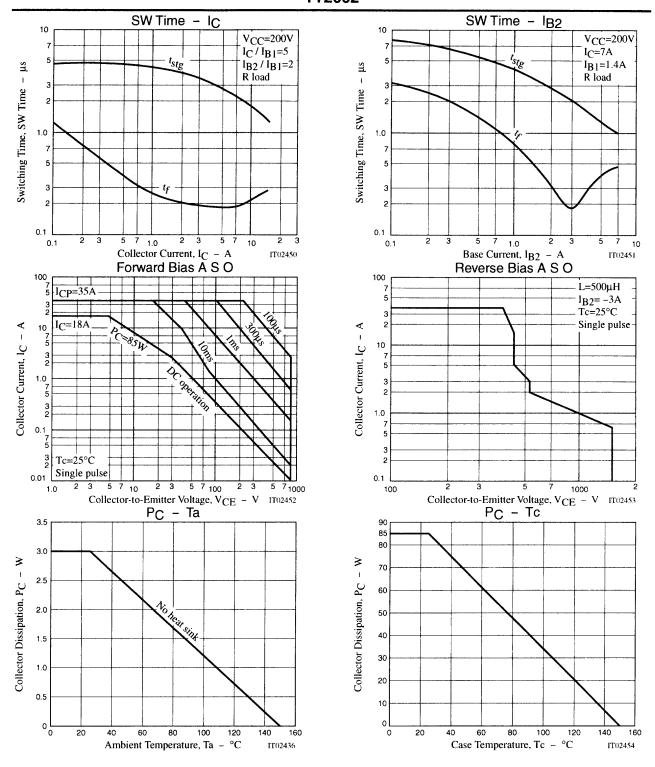
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 $I_C/I_B=5$

Switching Time Test Circuit







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