MOSPEC

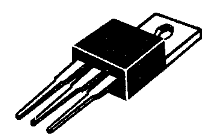
PNP SILICON POWER TRANSISTORS

2SA940 transistor is designed for use in general purpose power amplifier,vertical output application

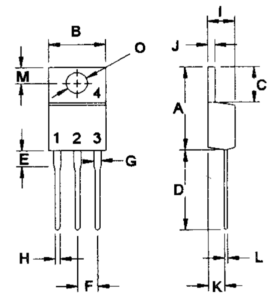
FEATURES:
* Collector-Emitter Voltage
  V_{CEO}= 150V(Min)
* DC Current Gain
  hFE= 40-140@I_C= 500mA
* Complementary NPN 2SC2073

PNP
2SA940

1.5 AMPERE
POWER
TRANASISTORS
150 VOLTS
25 WATTS



TO-220



PIN 1.BASE
2.COLLECTOR
3.EMITTER
4.COLLECTOR(CASE)

DIM	MILLIMETERS	
	MIN	MAX
A	14.68	15.31
B	9.78	10.42
C	5.01	6.52
D	13.06	14.62
E	3.57	4.07
F	2.42	3.66
G	1.12	1.36
H	0.72	0.96
I	4.22	4.98
J	1.14	1.38
K	2.20	2.97
L	0.33	0.55
M	2.48	2.98
O	3.70	3.90

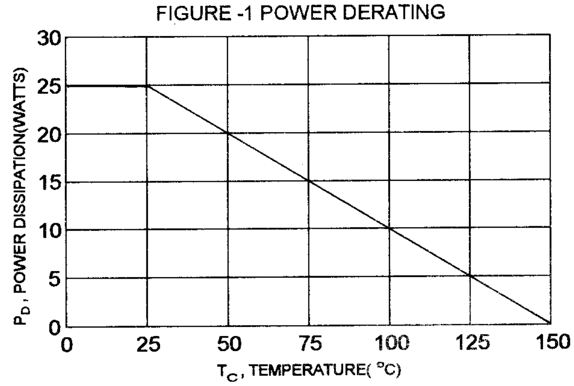
MAXIMUM RATINGS

Characteristic	Symbol	2SA940	Unit
Collector-Emitter Voltage	V_{CEO}	150	V
Collector-Base Voltage	V_{CBO}	150	V
Emitter-Base Voltage	V_{EBO}	5.0	V
Collector Current - Continuous - Peak	I_C I_{CM}	1.5 3.0	A
Base Current	I_B	0.5	A
Total Power Dissipation @T_C = 25°C Derate above 25°C	P_D	25 0.2	W W/°C
Operating and Storage Junction Temperature Range	T_J ,T_{STG}	-55 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance Junction to Case	Rθjc	5.0	°C/W

FIGURE -1 POWER DERATING

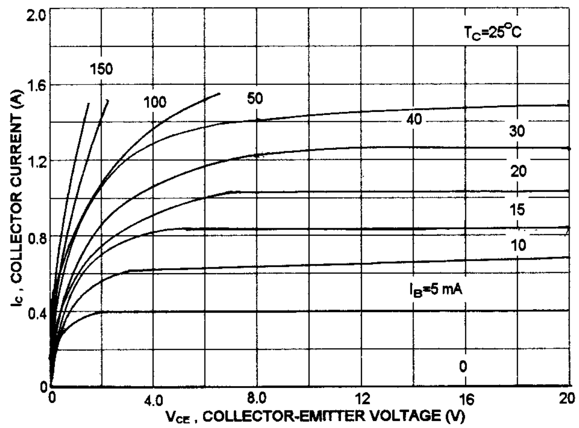


ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

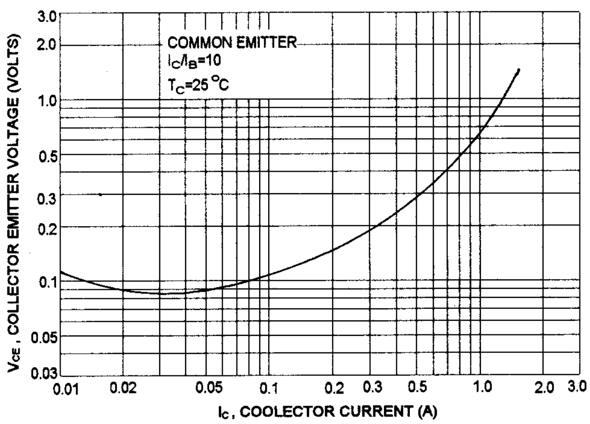
Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector-Base Voltage (I_C= 1.0 mA , I_B= 0)	V_{CBO}	150		V
Collector-Emitter Voltage (I_C= 5.0 mA, I_B= 0)	V_{CEO}	150		V
Emitter-Base Voltage (I_B= 1.0 mA, I_C= 0)	V_{EBO}	5.0		V
Collector Cutoff Current (V_{CB}= 120 V, I_E= 0)	I_{CBO}		10	uA
Emitter Cutoff Current (V_{EB}= 5.0 V, I_C= 0)	I_{EBO}		10	uA
ON CHARACTERISTICS (1)				
DC Current Gain (I_C= 0.5 A, V_{CE}= 10V)	hFE	40	140	
Collector-Emitter Saturation Voltage (I_C= 0.5 A, I_B= 50 mA)	$V_{CE(sat)}$		1.5	V
Base-Emitter On Voltage (I_C= 500 mA, V_{CE}= 5.0 V)	$V_{BE(on)}$	0.65	0.85	V
DYNAMIC CHARACTERISTICS				
Current-Gain-Bandwidth Product (I_C = 0.5 A, V_{CE} = 10 V, f = 1.0 MHz)	f_T	4.0		MHz

(1) Pulse Test: Pulse Width =300 μs, Duty Cycle ≦ 2.0%:

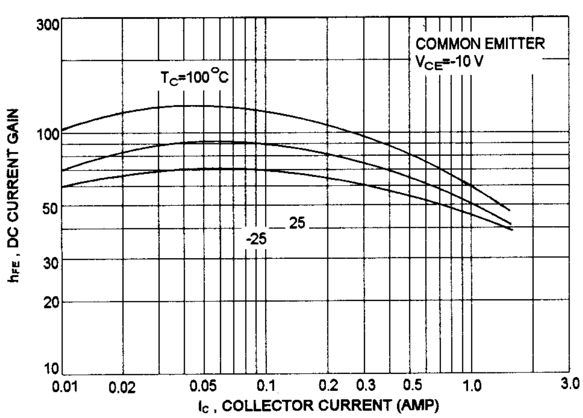
Ic - Vce



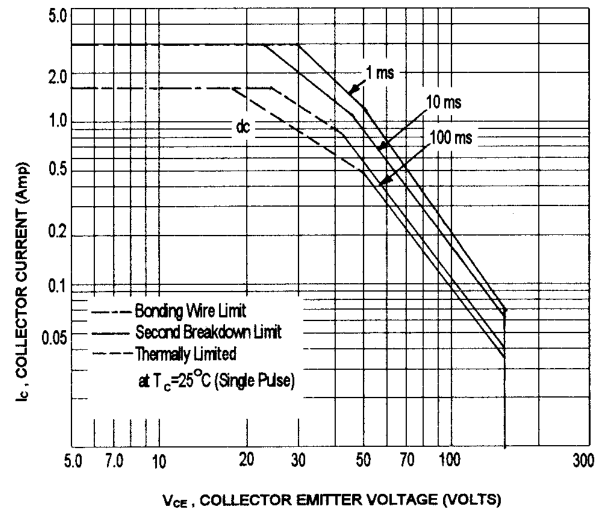
VCE(sat) - Ic



DC CURRENT GAIN



ACTIVE-REGION SAFE OPERATING AREA (SOA)



There are two limitation on the power handling ability of a transistor:average junction temperature and second breakdown safe operating area curves indicate I_C-V_{CE} limits of the transistor that must be observed for reliable operation i.e., the transistor must not be subjected to greater dissipation than curves indicate.

The data of SOA curve is base on $T_{J(PK)}$=150 °C;T_C is variable depending on conditions. second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(PK)} \leq$150°C,At high case temperatures, thermal limita - tion will reduce the power that can be handled to values less than the limitations imposed by second breakdown.