

Structure	:	Silicon Monolithic Integrated Circuit				
Product name	:	Pre-amplifier built-in ALC				
Туре	:	BA3308,BA3308F,BA3308FV				
Features	:	 Built-in ALC rectification diode Wide operating power supply voltage range (Vcc=4.5~14 V) Very little current consumption (lq=3.5mA) High gain (Gvo=80dB) Low distortion (THD=0.1%) Low noise (VNIN=1µVrms) Input coupling capacitor not needed Good ALC channel balance Built-in power supply mute circuit Dynamic range of ALC can be changed by attaching input resistance. 				

OAbsolute Maximum Ratings (Ta=25°C)

oltage			
	V _{cc}	16	V
3A3308		550 ^{*1}	mW
3A3308F	Pd	450 ^{*2}	
3A3308FV		350 ^{*3}	
Operating temperature		-25~+75	°C
Storage temperature		-55~+125	°C
1	3A3308 3A3308F 3A3308FV nperature	BA3308 BA3308F Pd BA3308FV nperature Topr	BA3308 550 *1 BA3308F Pd 450 *2 BA3308FV 350 *3 nperature Topr -25~+75

*1 Deratings is done at 5.5mW/°C above Ta=25°C

*² Deratings is done at 4.5mW/°C above Ta=25°C.

(When mounted on a 70mm × 70mm × 1.6mm PCB board)

 *3 Deratings is done at 3.5mW/°C $\,$ above Ta=25°C $\,$

OOperating Range (Ta=25°C)

Parameter	Symbol	Min	Тур	Max	Unit
Supply voltage	Vcc	4.5	-	14	V
WThis product is not designed for protection and instanting the sting					

*This product is not designed for protection against radioactive rays.

Application example

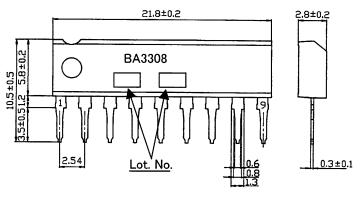
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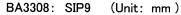


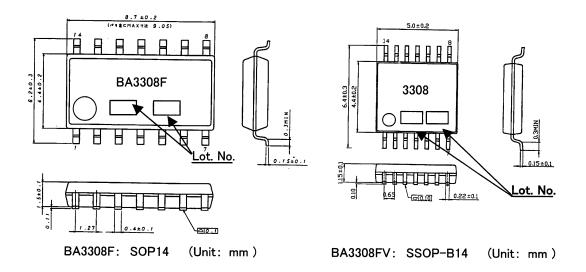
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Quiescent current	lq	1.5	3.5	4.5	mA	V _{IN} =0V _{rms}
Open loop voltage gain	G _{vo}	70	80	—	dB	V _{OUT} =-10dBV
Total harmonic distortion	THD	1	0.1	0.3	%	NAB34dB、V _{OUT} =40mV _{rms}
Input resistance	R _{IN}	15	25	45	kΩ	f=1kHz
Maximum output voltage	V _{OM}	0.6	1.2	-	V _{rms}	THD=1%
Input conversion noise voltage	V _{NIN}	-	1	2	μV _{rms}	Rg=2.2k Ω , Referenced to NAB 34dB at 1kHz
ALC range	ALC	40	45	-	dB	$Rg{=}3.9~k\Omega,~V_{IN}{=}{-}70dBV$ reference , THD=3%
ALC channel balance	ΔALC		0	2.5	dB	$V_{IN} = -60 dBV, -30 dBV$
Channel separation	CS	60	75	_	dB	f=1kHz、V _O =0dBV,NAB 34dB

OElectrical characteristics (Unless otherwise noted, Ta=25°C, Vcc=7.0V, f=1kHz, BPF20~20kHz)

OOuter dimensions

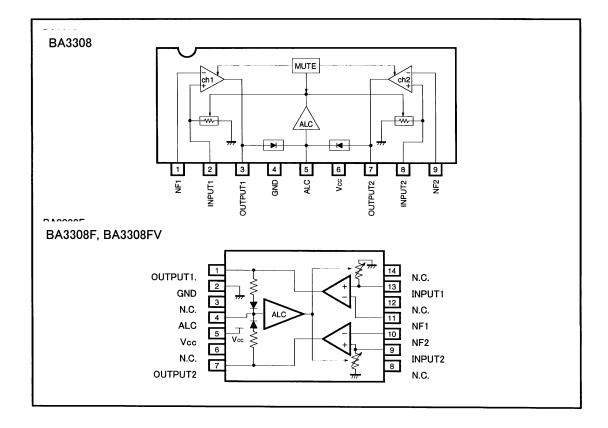








OBlock diagram



OPin number and pin name

BA3308	5
Pin No.	Pin name
1	NF1
2	INPUT1
3	OUTPUT1
4	GND
5	ALC
6	Vcc
7	OUTPUT2
8	INPUT2
9	NF2

BA3308F, BA3308FV

Pin No.	Pin name
1	OUTPUT1
2	GND
3	N.C.
4	ALC
5	Vcc
6	N.C.
7	OUTPUT2
8	N.C.
9	INPUT2
10	NF2
11	NF1
12	N.C.
13	INPUT1
14	N.C.



OCautions on use

1) Absolute maximum ratings

If applied voltage, operating temperature range, or other absolute maximum ratings are exceeded, the LSI may be damaged. Do not apply voltages or temperatures that exceed the absolute maximum ratings. If you think of a case in which absolute maximum ratings are exceeded, enforce fuses or other physical safety measures and investigate how not to apply the conditions under which absolute maximum ratings are exceeded to the LSI.

2) GND potential

Make the GND pin voltage such that it is the lowest voltage even when operating below it. Actually confirm that the voltage of each pin does not become a lower voltage than the GND pin, including transient phenomena.

3) Thermal design

Perform thermal design in which there are adequate margins by taking into account the allowable power dissipation in actual states of use.

4) Shorts between pins and miss-installation

When mounting the LSI on a board, pay adequate attention to orientation and placement discrepancies of the LSI. If it is miss-installed and the power is turned on, the LSI may be damaged. It also may be damaged if it is shorted by a foreign substance coming between pins of the LSI or between a pin and a power supply or a pin and a GND.

5) Operation in strong magnetic fields

Adequately evaluate use in a strong magnetic field, since there is a possibility of malfunction.

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