

QUAD ULTRA LOW-NOISE, WIDEBAND, OPERATIONAL AMPLIFIER

DESCRIPTION

The μ PC4574 is an ultra low noise, high slew rate quad operational amplifier specifically designed for audio, instrumentation, and communication circuits. The low noise and high frequency capabilities make it ideal for preamps and active filters for instrumentation and professional audio.

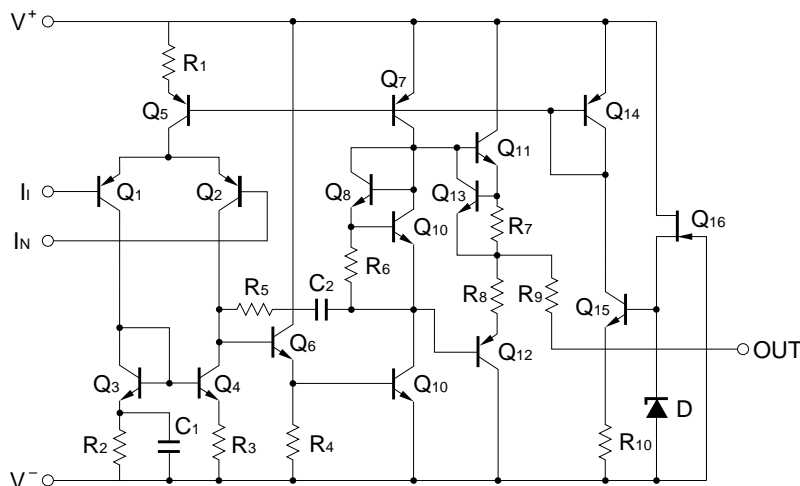
FEATURES

- Ultra low noise
- High slew rate
- Wide bandwidth
- Internal frequency compensation

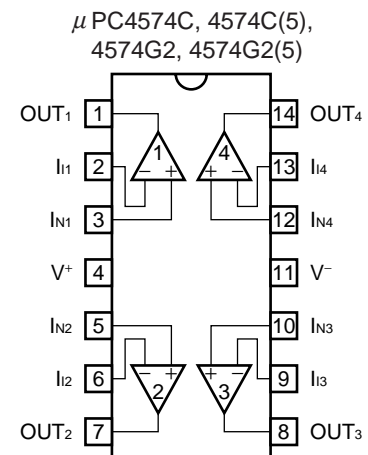
ORDERING INFORMATION

Part Number	Package
μ PC4574C	14-pin plastic DIP (7.62 mm (300))
μ PC4574C(5)	14-pin plastic DIP (7.62 mm (300))
μ PC4574G2	14-pin plastic SOP (5.72 mm (225))
μ PC4574G2(5)	14-pin plastic SOP (5.72 mm (225))

EQUIVALENT CIRCUIT (1/4 Circuit)



PIN CONFIGURATION (Top View)



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ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$)

Parameter		Symbol	Ratings	Unit
Voltage between V^+ and V^- ^{Note1}		$V^+ - V^-$	-0.3 to +36	V
Differential Input Voltage		V_{ID}	± 30	V
Input Voltage ^{Note2}		V_I	$V^- - 0.3$ to $V^+ + 0.3$	V
Output Voltage ^{Note3}		V_O	$V^- - 0.3$ to $V^+ + 0.3$	V
Power Dissipation	C Package ^{Note4}	P_T	570	mW
	G2 Package ^{Note5}		550	mW
Output Short Circuit Duration ^{Note6}			10	sec
Operating Ambient Temperature		T_A	-20 to +80	$^\circ\text{C}$
Storage Temperature		T_{stg}	-55 to +125	$^\circ\text{C}$

Notes 1. Reverse connection of supply voltage can cause destruction.

2. The input voltage should be allowed to input without damage or destruction. Even during the transition period of supply voltage, power on/off etc., this specification should be kept. The normal operation will establish when the both inputs are within the Common Mode Input Voltage Range of electrical characteristics.
3. This specification is the voltage which should be allowed to supply to the output terminal from external without damage or destructive. Even during the transition period of supply voltage, power on/off etc., this specification should be kept. The output voltage of normal operation will be the Output Voltage Swing of electrical characteristics.
4. Thermal derating factor is $-7.6 \text{ mW}/^\circ\text{C}$ when ambient temperature is higher than 50°C .
5. Thermal derating factor is $-5.5 \text{ mW}/^\circ\text{C}$ when ambient temperature is higher than 25°C .
6. Pay careful attention to the total power dissipation not to exceed the absolute maximum ratings, Note 4 and Note 5.

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage	V^\pm	± 4		± 16	V
Output Current	I_O			± 10	mA
Source Resistance	R_S			50	$\text{k}\Omega$
Capacitive Load ($A_v = +1$)	C_L			100	pF

μ PC4574C, μ PC4574G2ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, $V^\pm = \pm 15\text{ V}$)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Input Offset Voltage	V_{IO}	$R_S \leq 50\ \Omega$		± 0.3	± 5	mV
Input Offset Current ^{Note 7}	I_{IO}			± 10	± 200	nA
Input Bias Current ^{Note 7}	I_B			500	1000	nA
Large Signal Voltage Gain	A_V	$R_L \geq 2\ \text{k}\Omega$, $V_O = \pm 10\text{ V}$	30000	300000		
★ Supply Current ^{Note 8}	I_{CC}	$I_O = 0\text{ A}$		8.5	12	mA
Common Mode Rejection Ratio	CMR		80	100		dB
Supply Voltage Rejection Ratio	SVR		80	100		dB
Output Voltage Swing	V_{OM}	$R_L \geq 10\ \text{k}\Omega$	± 12	± 13.4		V
		$R_L \geq 2\ \text{k}\Omega$	± 10	$+12.8$ -12.4		
Common Mode Input Voltage Range	V_{ICM}		± 12	± 14		V
Slew Rate	SR	$R_L \geq 2\ \text{k}\Omega$	4	6		V/ μs
Gain Band Width Product	GBW	$f_o = 100\ \text{kHz}$	10	14		MHz
Unity Gain Frequency	f_{unity}	open loop		7		MHz
Phase Margin	ϕ_{unity}	open loop		50		degree
Total Harmonic Distortion	THD	$V_O = 3\ V_{r.m.s.}$, $f = 20\ \text{Hz}$ to $20\ \text{kHz}$ (Fig.1)		0.002		%
Input Equivalent Noise Voltage	V_n	RIAA (Fig.2)		1.2		$\mu\text{V}_{r.m.s.}$
		FLAT+JIS A, $R_S = 100\ \Omega$ (Fig.3)		0.53	0.65	
Input Equivalent Noise Voltage Density	e_n	$f_o = 10\ \text{Hz}$, $R_S = 100\ \Omega$		5.5		$\text{nV}/\sqrt{\text{Hz}}$
		$f_o = 1\ \text{kHz}$, $R_S = 100\ \Omega$		5.0		
Input Equivalent Noise Current Density	i_n	$f_o = 1\ \text{kHz}$		0.7		$\text{pA}/\sqrt{\text{Hz}}$
Channel Separation		$f = 20\ \text{Hz}$ to $20\ \text{kHz}$		120		dB

Notes 7. Input bias currents flow out from IC. Because each currents are base current of PNP-transistor on input stage.

★ **8.** This current flows irrespective of the existence of use.

μPC4574C(5), μPC4574G2(5)

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, $V^\pm = \pm 15\text{V}$)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Input Offset Voltage	V_{IO}	$R_S \leq 50 \Omega$		± 0.3	± 1	mV
Input Offset Current ^{Note 7}	I_{IO}			± 10	± 60	nA
Input Bias Current ^{Note 7}	I_B			500	650	nA
Large Signal Voltage Gain	A_V	$R_L \geq 2 \text{ k}\Omega$, $V_O = \pm 10 \text{ V}$	50000	300000		
★ Supply Current ^{Note 8}	I_{CC}	$I_O = 0 \text{ A}$		8.5	11	mA
Common Mode Rejection Ratio	CMR		85	100		dB
Supply Voltage Rejection Ratio	SVR		85	100		dB
Output Voltage Swing	V_{OM}	$R_L \geq 10 \text{ k}\Omega$	± 13	± 13.4		V
		$R_L \geq 2 \text{ k}\Omega$	± 11.5	$+12.8$ -12.4		
Common Mode Input Voltage Range	V_{ICM}		± 13	± 14		V
Slew Rate	SR	$R_L \geq 2 \text{ k}\Omega$	4	6		V/ μs
Gain Band Width Product	GBW	$f_O = 100 \text{ kHz}$	10	14		MHz
Unity Gain Frequency	f_{unity}	open loop		7		MHz
Phase Margin	ϕ_{unity}	open loop		50		degree
Total Harmonic Distortion	THD	$V_O = 3 V_{r.m.s.}$, $f = 20 \text{ Hz to } 20 \text{ kHz}$ (Fig.1)		0.002		%
Input Equivalent Noise Voltage	V_n	RIAA (Fig.2)		1.2		$\mu\text{V}_{r.m.s.}$
		FLAT+JIS A, $R_S = 100 \Omega$ (Fig.3)		0.53	0.65	
Input Equivalent Noise Voltage Density	e_n	$f_O = 10 \text{ Hz}$, $R_S = 100 \Omega$		5.5		$\text{nV}/\sqrt{\text{Hz}}$
		$f_O = 1 \text{ kHz}$, $R_S = 100 \Omega$		5.0		
Input Equivalent Noise Current Density	i_n	$f_O = 1 \text{ kHz}$		0.7		$\text{pA}/\sqrt{\text{Hz}}$
Channel Separation		$f = 20 \text{ Hz to } 20 \text{ kHz}$		120		dB

Notes 7. Input bias currents flow out from IC. Because each currents are base current of PNP-transistor on input stage.

★ **8.** This current flows irrespective of the existence of use.

MEASUREMENT CIRCUIT

Fig.1 Total Harmonic Distortion Measurement Circuit

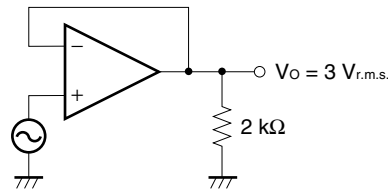


Fig.2 Noise Measurement Circuit (RIAA)

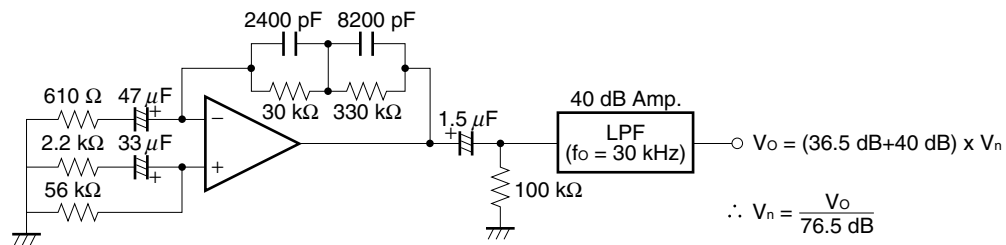
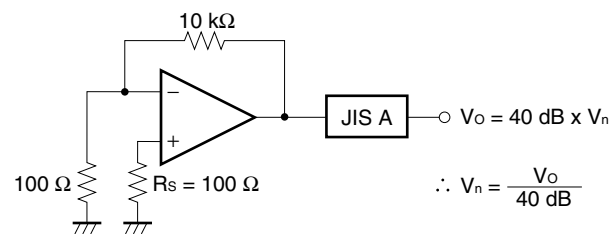
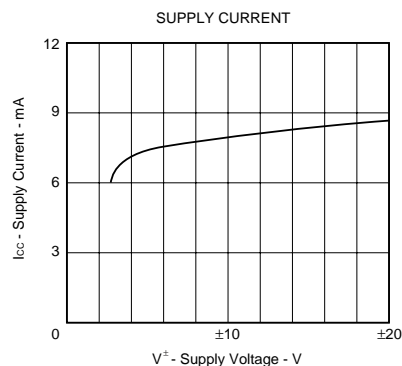
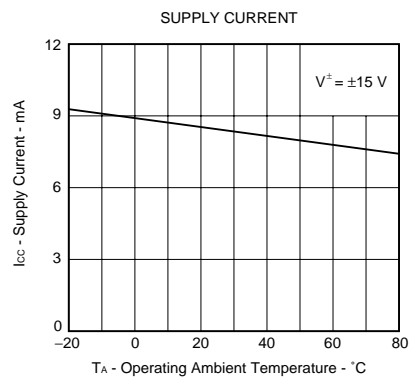
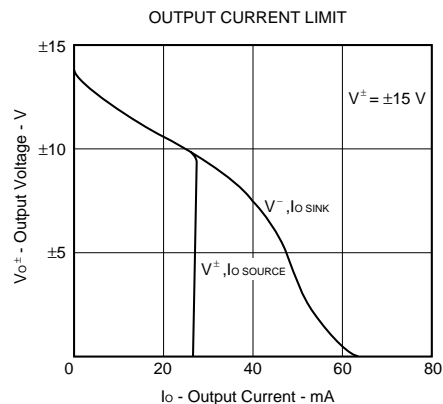
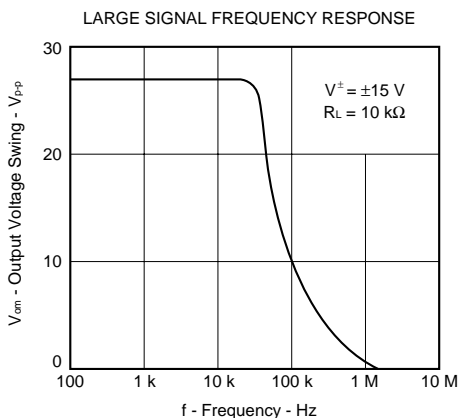
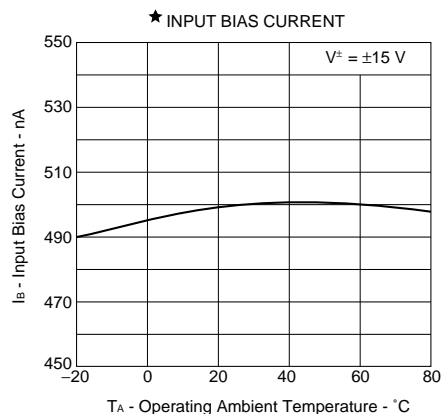
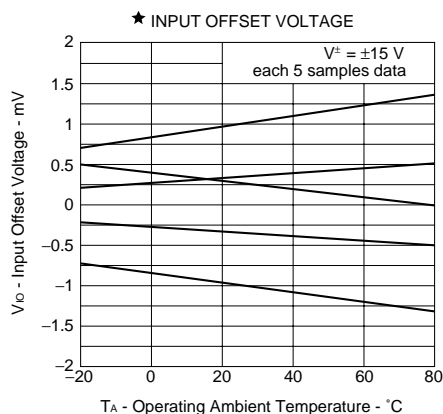
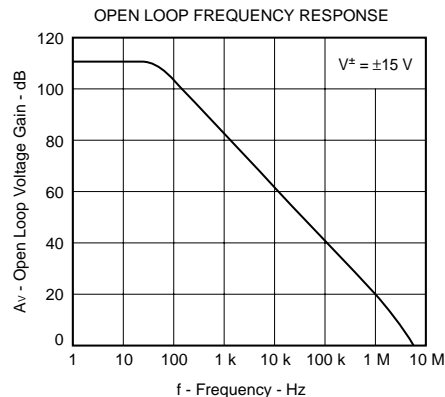
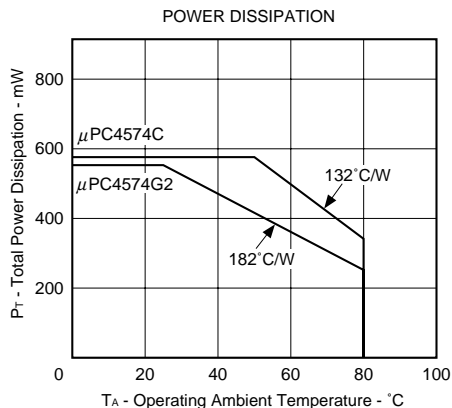
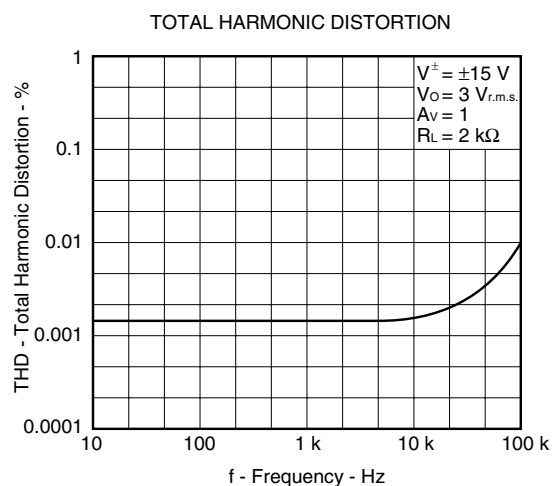
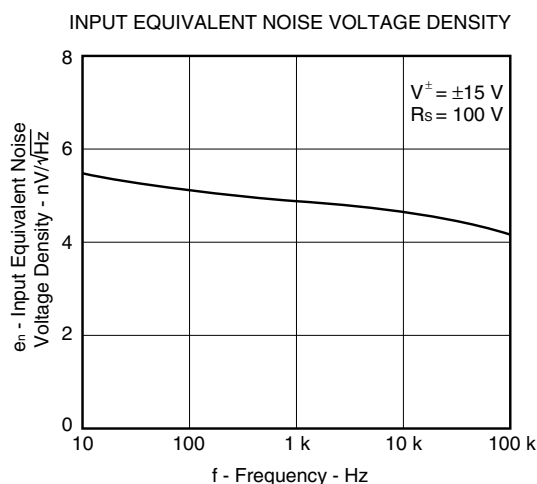
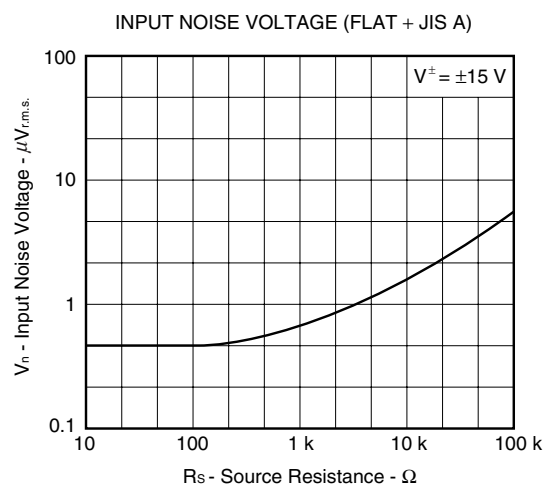
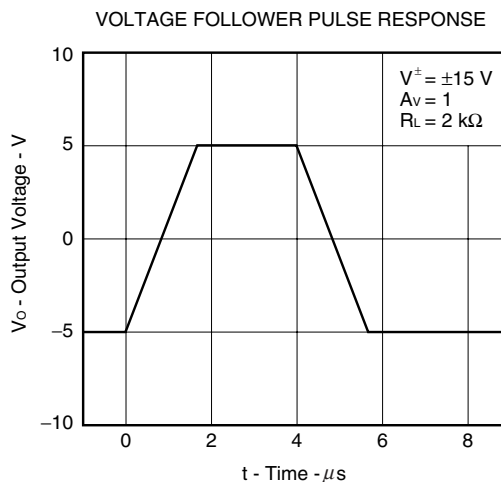
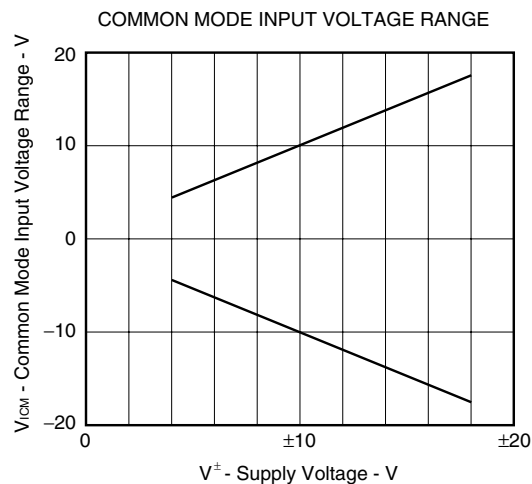


Fig.3 Flat Noise Measurement Circuit (FLAT+JIS A)



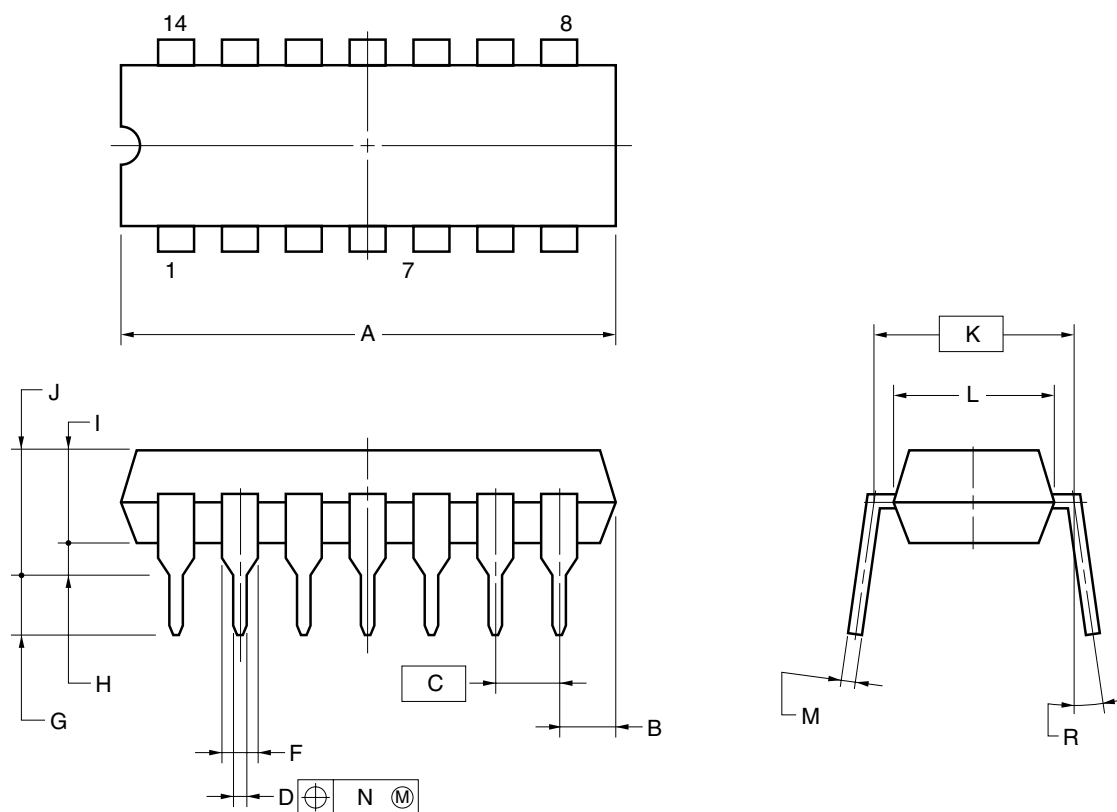
TYPICAL PERFORMANCE CHARACTERISTICS ($T_A = 25^\circ\text{C}$, TYP.)





PACKAGE DRAWINGS (Unit: mm)

14-PIN PLASTIC DIP (7.62 mm (300))



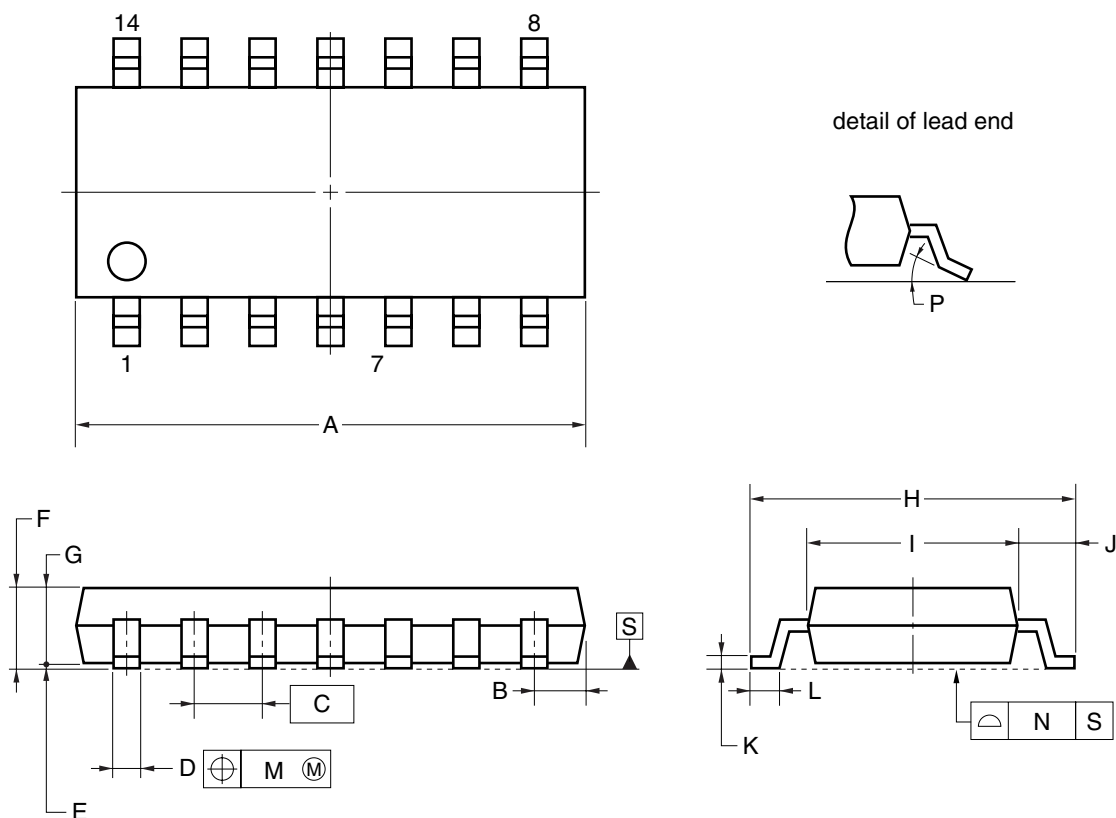
NOTES

- Each lead centerline is located within 0.25 mm of its true position (T.P.) at maximum material condition.
- Item "K" to center of leads when formed parallel.

ITEM	MILLIMETERS
A	19.22±0.2
B	2.14 MAX.
C	2.54 (T.P.)
D	0.50±0.10
F	1.32±0.12
G	3.6±0.3
H	0.51 MIN.
I	3.55
J	4.3±0.2
K	7.62 (T.P.)
L	6.4±0.2
M	0.25 ^{+0.10} _{-0.05}
N	0.25
R	0~15°

P14C-100-300B1-3

14-PIN PLASTIC SOP (5.72 mm (225))



NOTE

Each lead centerline is located within 0.1 mm of its true position (T.P.) at maximum material condition.

ITEM	MILLIMETERS
A	10.2±0.26
B	1.42 MAX.
C	1.27 (T.P.)
D	0.42 ^{+0.08} _{-0.07}
E	0.1±0.1
F	1.59 ^{+0.21} _{-0.2}
G	1.49
H	6.5±0.2
I	4.4±0.1
J	1.1±0.16
K	0.17 ^{+0.08} _{-0.07}
L	0.6±0.2
M	0.1
N	0.10
P	3° ^{+7°} _{-3°}

S14GM-50-225B, C-6

★ RECOMMENDED SOLDERING CONDITIONS

The μPC4574 should be soldered and mounted under the following recommended conditions.

For soldering methods and conditions other than those recommended below, contact an NEC Electronics sales representative.

For technical information, see the following website.

Semiconductor Device Mount Manual (<http://www.necel.com/pkg/en/mount/index.html>)

Type of Surface Mount Device

μPC4574G2, 4574G2(5): 14-pin plastic SOP (5.72 mm (225))

Process	Conditions	Symbol
Infrared Ray Reflow	Peak temperature: 230°C or below (Package surface temperature), Reflow time: 30 seconds or less (at 210°C or higher), Maximum number of reflow processes: 1 time.	IR30-00-1
Vapor Phase Soldering	Peak temperature: 215°C or below (Package surface temperature), Reflow time: 40 seconds or less (at 200°C or higher), Maximum number of reflow processes: 1 time.	VP15-00-1
Wave Soldering	Solder temperature: 260°C or below, Flow time: 10 seconds or less, Maximum number of flow processes: 1 time, Pre-heating temperature: 120°C or below (Package surface temperature).	WS60-00-1
Partial Heating Method	Pin temperature: 300°C or below, Heat time: 3 seconds or less (Per each side of the device).	—

Caution Apply only one kind of soldering condition to a device, except for "partial heating method", or the device will be damaged by heat stress.

Type of Through-hole Device

μPC4574C, 4574C(5): 14-pin plastic DIP (7.62 mm (300))

Process	Conditions
Wave Soldering (only to leads)	Solder temperature: 260°C or below, Flow time: 10 seconds or less.
Partial Heating Method	Pin temperature: 300°C or below, Heat time: 3 seconds or less (per each lead).

Caution For through-hole device, the wave soldering process must be applied only to leads, and make sure that the package body does not get soldered.

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