



# Video Switch for TV/VCR Use

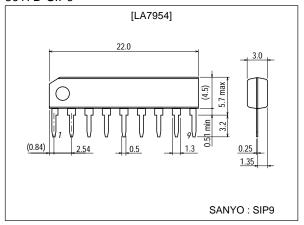
## **Features**

- 4 inputs, 1 output.
- Excellent crosstalk characteristic.
- Wide band.

# **Package Dimensions**

unit:mm

3017D-SIP9



## **Specifications**

#### **Maximum Ratings** at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>7</sub> max		14	V
Maximum input supply voltage 1	V <sub>4</sub> max, V <sub>6</sub> max, V <sub>8</sub> max, V <sub>9</sub> max		8	V
Maximum input supply voltage 2	V <sub>2</sub> max, V <sub>3</sub> max	V <sub>CC</sub> =14V	14	V
Maximum output current	I <sub>1</sub> max		7	mA
Allowable power dissipation	Pd max	Ta≤65°C	540	mW
Operating temperature	Topr		-20 to +65	°C
Storage temperature	Tstg		-55 to +150	°C

## **Operating Conditions** at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Operating voltage range	V <sub>CC</sub> op		8 to 13.5	V
Recommended supply voltage	Vcc		12	V

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# Operating Characteristics at $Ta=25\,^{\circ}C,\,V_{CC}\!\!=\!\!12V$

Parameter	Symbol	Conditions		Ratings		
	Symbol		min	typ	max	Unit
Quiescent current drain	lcc		9	12	17	mA
Input bias voltage	V <sub>4</sub> , V <sub>6</sub> , V <sub>8</sub> , V <sub>9</sub>		4.5	4.8	5.1	V
Output bias voltage	V <sub>1</sub>		3.7	4.1	4.3	V
Output DC offset voltage	Vos		-50	0	+50	mV
Control threshold voltage	V <sub>2H</sub> , V <sub>3H</sub> ,		2.3			V
	V <sub>2L</sub> , V <sub>3L</sub>				0.7	V
Control input current	l <sub>2</sub> , l <sub>3</sub>		-20	-6		μΑ
Voltage gain	GV	f=1MHz, V <sub>IN</sub> =2Vp-p (Note 1)	-0.5	-0.2		dB
Frequency characteristic	GV-f	0dB at f=100kHz (Note 1) f=10MHz, V <sub>IN</sub> =2Vp-p	-3	0		dB
Output dynamic range	V <sub>DR</sub>	f=15kHz, V <sub>IN</sub> =6Vp-p (Note 1)		0.3	6	%
Crosstalk (Note 2)		V <sub>IN</sub> =2Vp-p, f=3MHz (Note 1)	48	58		dB
	C <sub>T</sub>	V <sub>IN</sub> =2Vp-p, f=5MHz (Note 1)	45	55		dB

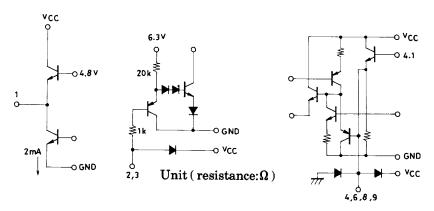
<sup>\*</sup> Current flowing into the IC is defined as positive; current flowing out is defined as negative.

### **Video Switch Truth Table**

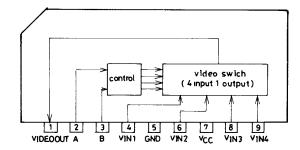
S2 (Pin 2)	S3 (Pin 3)	VIN1 (Pin 4)	VIN2 (Pin 6)	VIN3 (Pin 8)	V <sub>IN4</sub> (Pin 9)
Н	Н	ON	OFF	OFF	OFF
L	Н	OFF	ON	OFF	OFF
Н	L	OFF	OFF	ON	OFF
L	L	OFF	OFF	OFF	ON

Note 1: Refer to this Truth Table and make measurements by switching S2, S3.

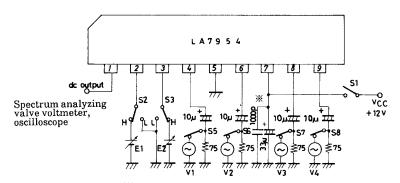
## Input/Output Equivalent Circuit



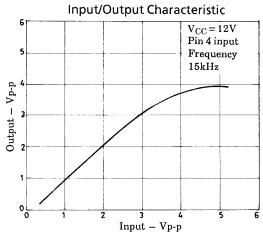
## **Equivalent Circuit Block Diagram**

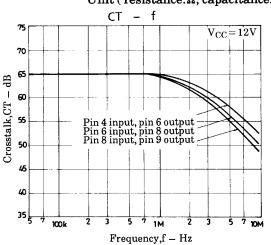


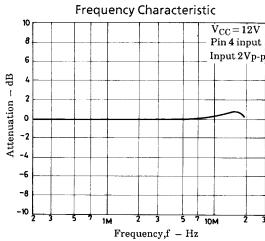
### **Test Circuit**



% : Connect the bypass capacitor for  $V_{CC}$  as close to pin 7 as possible.  $Unit \ (\ resistance: \pmb{\Omega}, \ capacitance: \pmb{F}\ )$ 







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