

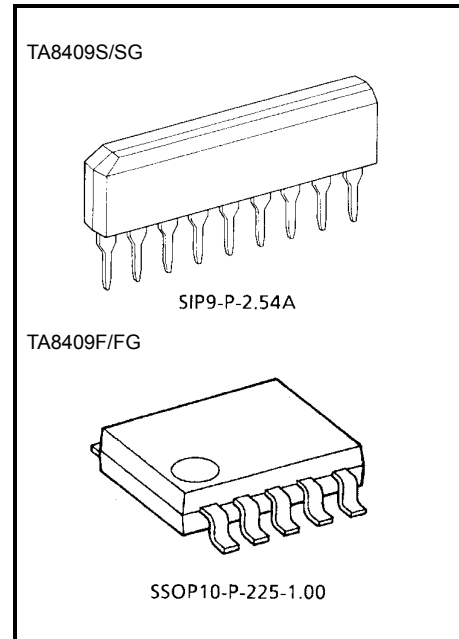
TA8409S/SG, TA8409F/FG

Bridge Driver

TA8409S/SG and TA8409F/FG are bridge driver with output voltage control.

Features

- Modes available (CW/CCW/STOP/BRAKE)
- Output current up to 0.4 A (AVE) and 1.0 A (PEAK)
- Wide range of operating voltage
 V_{CC} (opr.) = 4.5 to 20 V
 V_S (opr.) = 0 to 20 V
 *Please consider the internal loss (V_{sat}) to operate the IC though minimum V_S is defined zero.
 V_{ref} (opr.) = 0 to 20 V ($V_{ref} \leq V_S$)
- Built-in thermal shutdown
- Standby mode available (STOP MODE)
- Hysteresis for all inputs.



Weight

SIP9-P-2.54A: 0.92 g (typ.)

SSOP10-P-225-1.00: 0.09 g (typ.)

TA8409SG/FG:

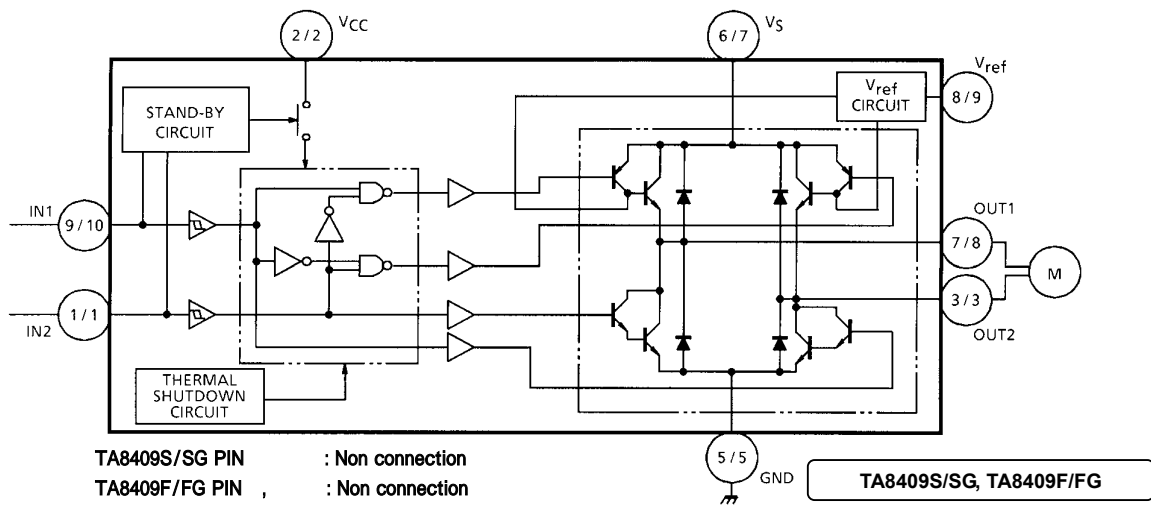
TA8409SG/FG is Pb free product.

The following conditions apply to solderability:

*Solderability

1. Use of Sn-37Pb solder bath
 *solder bath temperature=230 degrees
 *dipping time=5seconds
 *number of times=once
 *use of R-type flux
2. Use of Sn-3.0Ag-0.5Cu solder bath
 *solder bath temperature=245 degrees
 *dipping time=5seconds
 *the number of times=once
 *use of R-type flux

Block Diagram



Pin Function

TA8409S/SG

Pin No.	Symbol	Functional Description
1	IN2	Input terminal
2	V _{CC}	Supply voltage terminal for logic
3	OUT2	Output terminal
4	NC	Non connection
5	GND	GND terminal
6	V _S	Supply voltage terminal for motor driver
7	OUT1	Output terminal
8	V _{ref}	Reference voltage terminal for control circuit
9	IN1	Input terminal

TA8409F/FG

Pin No.	Symbol	Functional Description
1	IN2	Input terminal
2	V _{CC}	Supply voltage terminal for logic
3	OUT2	Output terminal
4	NC	Non connection
5	GND	GND terminal
6	NC	Non connection
7	V _S	Supply voltage terminal for motor driver
8	OUT1	Output terminal
9	V _{ref}	Reference voltage terminal for control circuit.
10	IN1	Input terminal

Function

Input		Output		Mode
IN 1	IN 2	OUT1	OUT2	MB
0	0	∞	∞	STOP
1	0	H	L	CW/CCW
0	1	L	H	CCW/CW
1	1	L	L	BRAKE

∞: High impedance

Note: Inputs are all high active type.

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Supply voltage		V _{CC}	25	V
Motor drive voltage		V _S	25	V
Reference voltage		V _{ref}	25	V
Output current	PEAK	I _O (PEAK)	1.0	A
	AVE.	I _O (AVE.)	0.4	
Power dissipation	TA8409F/FG	P _D	0.735 (Note)	W
	TA8409S/SG		0.95	
Operating temperature		T _{opr}	-30 to 75	°C
Storage temperature		T _{stg}	-55 to 150	°C

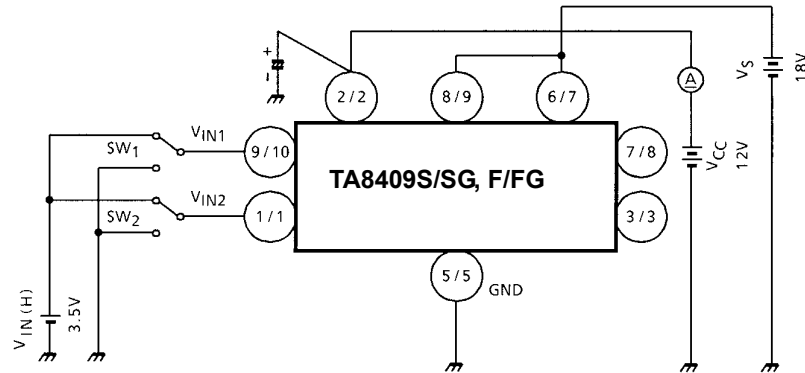
Note: This rating is obtained by mounting on 50 × 50 × 1.6 mm PCB that occupied above 30% of copper area.

Electrical Characteristics (Ta = 25°C, V_{CC} = 12 V, V_S = 18 V)

Characteristics		Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Supply current		I _{CC1}	1	Output OFF, CW/CCW mode	—	10.0	15.0	mA
		I _{CC2}	1	Output OFF, STOP mode	—	0	50	μA
		I _{CC3}	1	Output OFF, BREAK mode	—	6.5	10.0	mA
Input operating voltage	1 (High)	V _{IN1}	2	T _j = 25°C IN1, 2	3.5	—	5.5	V
	2 (Low)	V _{IN2}	2	T _j = 25°C IN1, 2	GND	—	0.8	
Input current		I _{IN}	2	Sink mode, V _{IN} = 3.5 V	—	3	10	μA
Input hysteresis voltage		ΔV _T	2	—	—	0.7	—	V
Saturation voltage	Upper side	V _{SAT U-1}	3	V _{ref} = V _S , V _{OUT} -V _S measure I _O = 0.2 A, CW/CCW mode	—	0.9	1.2	V
	Lower side	V _{SAT L-1}	3	V _{ref} = V _S , V _{OUT} -GND measure I _O = 0.2 A, CW/CCW mode	—	0.8	1.2	
	Upper side	V _{SAT U-2}	3	V _{ref} = V _S , V _{OUT} -V _S measure I _O = 0.4 A, CW/CCW mode	—	1.0	1.35	
	Lower side	V _{SAT L-2}	3	V _{ref} = V _S , V _{OUT} -GND measure I _O = 0.4 A, CW/CCW mode	—	0.9	1.35	
Output voltage		V _{SAT U-1'}	3	V _{ref} = 10 V, V _{OUT} -GND measure I _O = 0.2 A	10.4	11.2	12.2	V
		V _{SAT U-2'}	3	V _{ref} = 10 V, V _{OUT} -GND measure I _O = 0.4 A	—	10.9	—	
Output transistor leakage current	Upper side	I _{LU}	4	V _L = 25 V	—	—	50	μA
	Lower side	I _{LL}	4	V _L = 25 V	—	—	50	
Diode forward voltage	Upper side	V _{F U-1}	5	I _F = 0.4 A	—	1.5	—	V
	Lower side	V _{F L-1}	5	I _F = 0.4 A	—	0.9	—	
Reference current		I _{ref}	2	V _{ref} = 10 V, source mode	—	20	40	μA

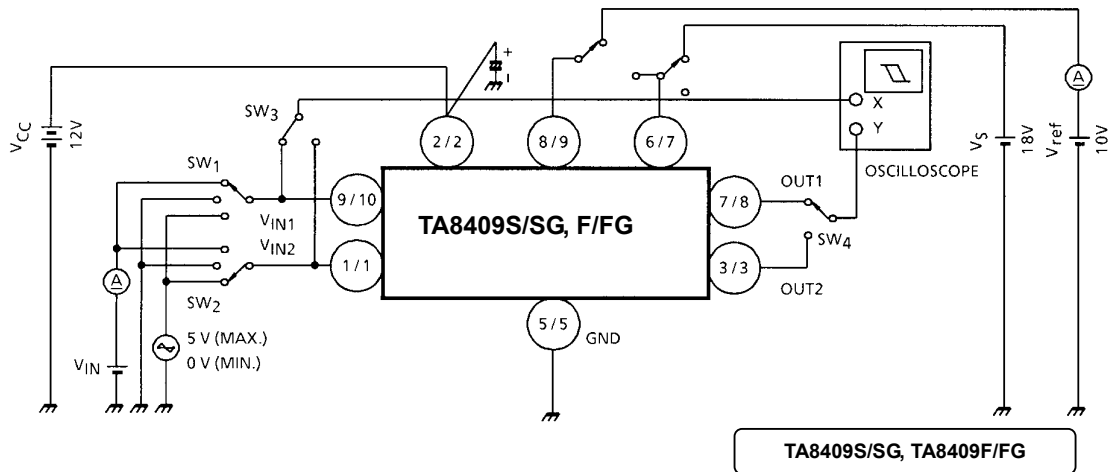
Test Circuit 1

I_{CC1} , I_{CC2} , I_{CC3}



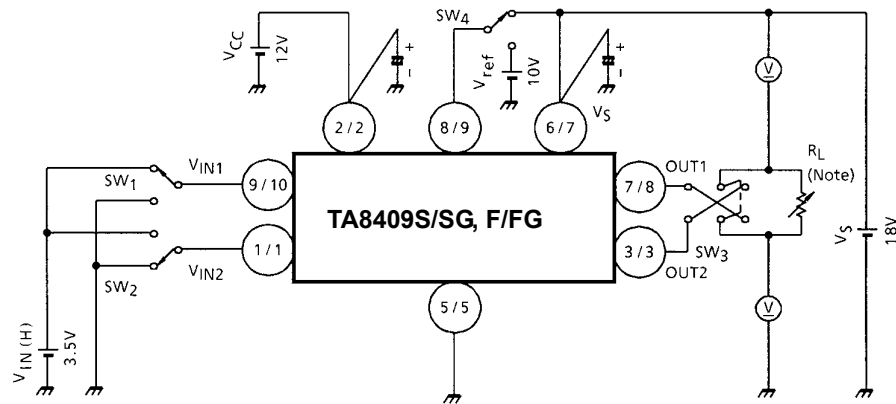
Test Circuit 2

V_{IN1} , V_{IN2} , I_{IN} , ΔV_T , I_{ref}



Test Circuit 3

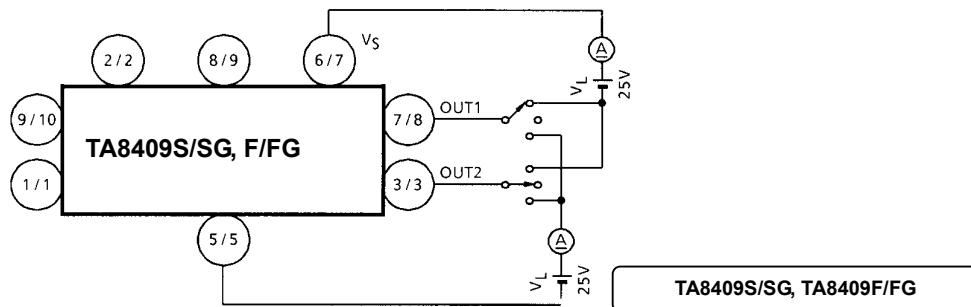
$V_{SAT\ U-1, 2}$, $V_{SAT\ L-1, 2}$, $V_{SAT\ U-1', 2'}$



Note: Calibrate I_{OUT} to 0.2/0.4 A by R_L .

Test Circuit 4

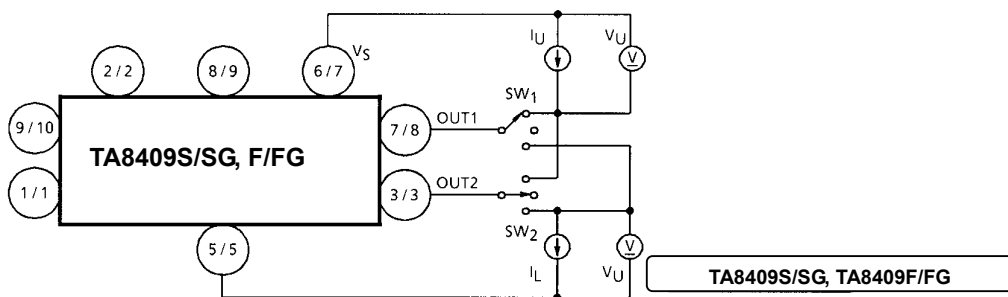
$I_{L\ U, L}$



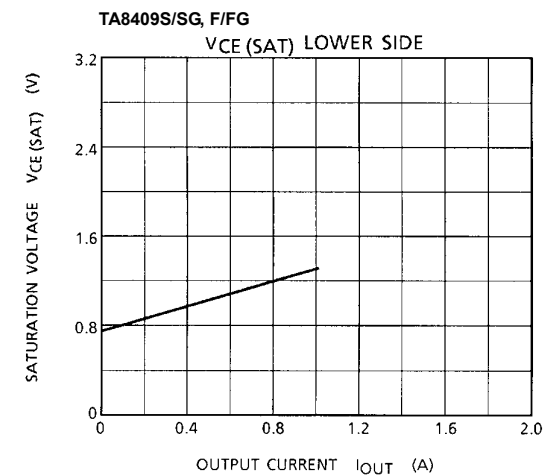
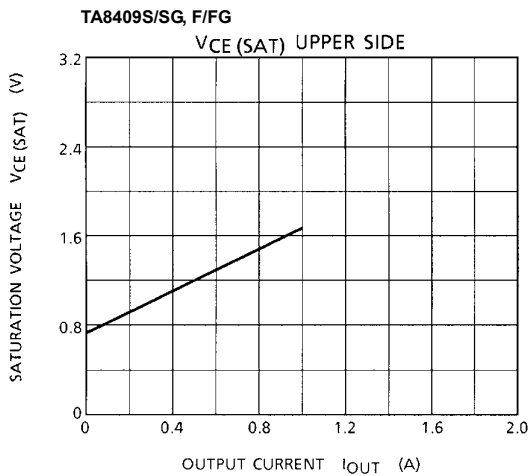
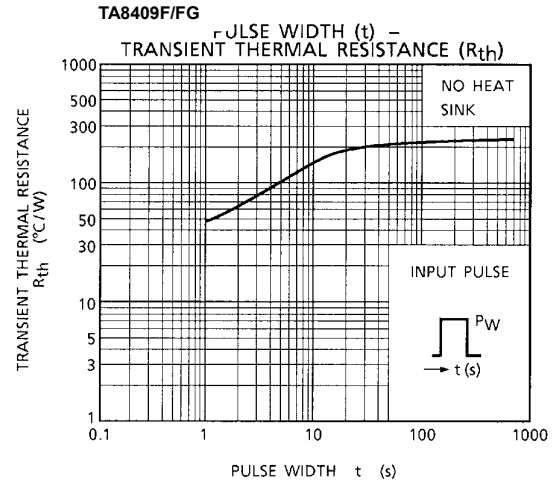
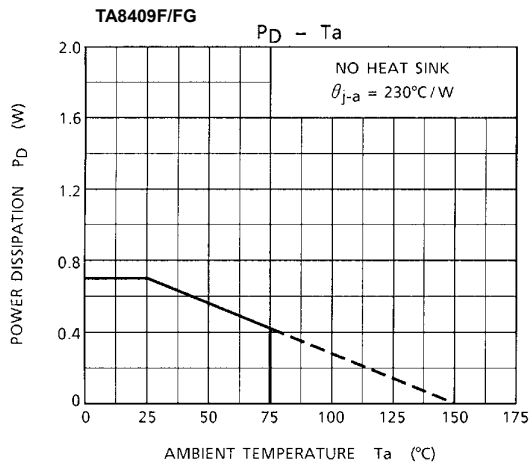
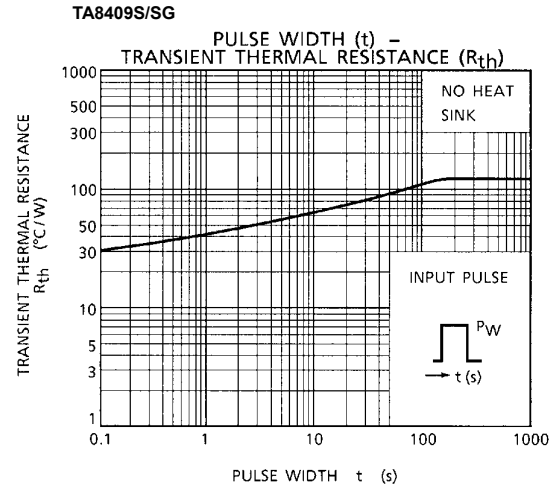
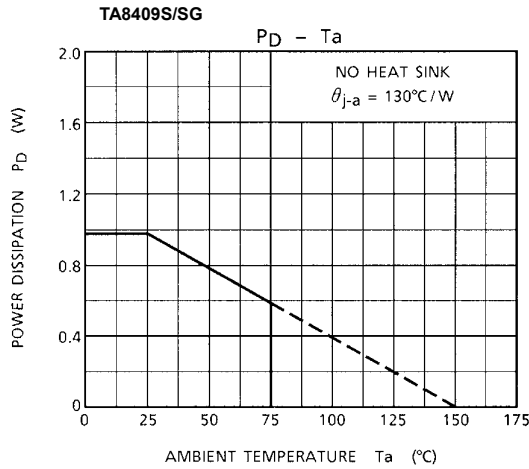
TA8409S/SG, TA8409F/FG

Test Circuit 5

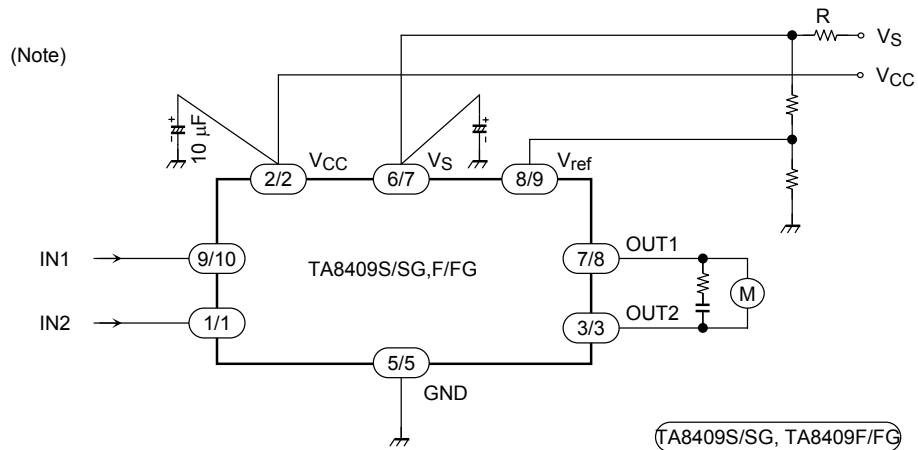
$V_F\ U-1, 2$, $V_F\ L-1, 2$



TA8409S/SG, TA8409F/FG



Application Circuit



Note 1: Attach a bypass capacitor to the VS pin as required.

Note 2: Utmost care is necessary in the design of the output, V_{CC}, V_M, and GND lines since the IC may be destroyed by short-circuiting between outputs, air contamination faults, or faults due to improper grounding, or by short-circuiting between contiguous pins.

Note 3: Switching the inputs may allow a pass-through current to flow. Keep the IC device in the STOP mode (for at least 100 μs) during the switching. Alternatively, insert a current limiting resistor R.

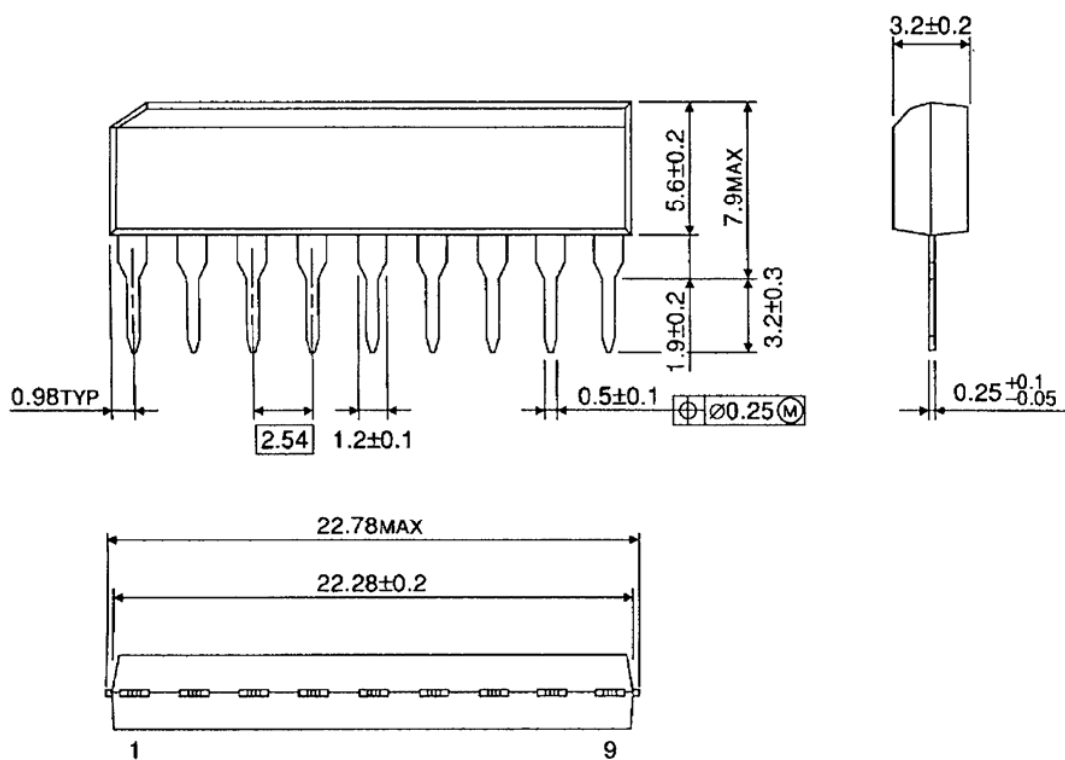
Note 4: Use a current limiting resistor R or fuse for overcurrent protection.

Note 5: When turning on the power for the IC device, apply V_S after V_{CC} (or V_{CC} and V_S simultaneously).
When shutting off the power, drop V_S before V_{CC} (or V_S and V_{CC} simultaneously).
When turning on the power (V_{CC}), keep both the inputs (IN1 and IN2) on a low level.

Package Dimensions

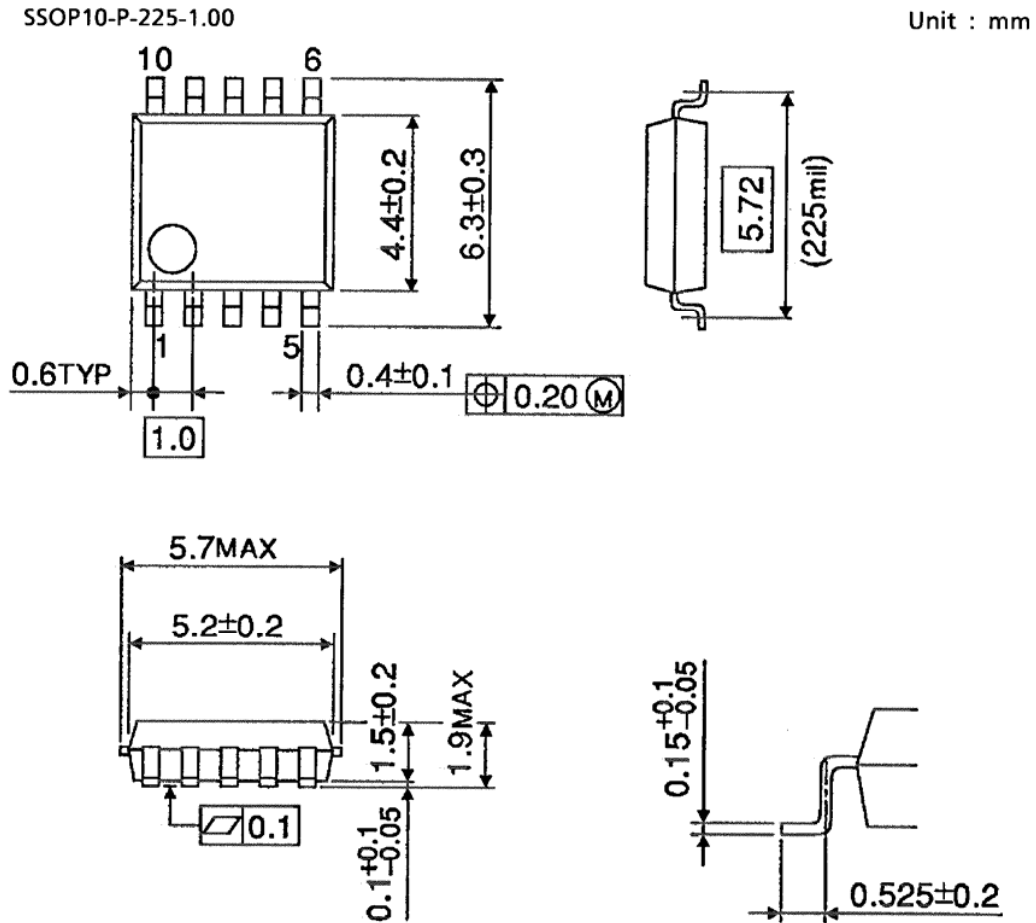
SIP9-P-2.54A

Unit : mm



Weight: 0.92 g (typ.)

Package Dimensions



Weight: 0.09 g (typ.)

Notes on Contents**1. Block Diagrams**

Some of the functional blocks, circuits, or constants in the block diagram may be omitted or simplified for explanatory purposes.

2. Equivalent Circuits

The equivalent circuit diagrams may be simplified or some parts of them may be omitted for explanatory purposes.

3. Timing Charts

Timing charts may be simplified for explanatory purposes.

4. Application Circuits

The application circuits shown in this document are provided for reference purposes only. Thorough evaluation is required, especially at the mass production design stage.

Toshiba does not grant any license to any industrial property rights by providing these examples of application circuits.

5. Test Circuits

Components in the test circuits are used only to obtain and confirm the device characteristics. These components and circuits are not guaranteed to prevent malfunction or failure from occurring in the application equipment.

IC Usage Considerations**Notes on handling of ICs**

- [1] The absolute maximum ratings of a semiconductor device are a set of ratings that must not be exceeded, even for a moment. Do not exceed any of these ratings.
Exceeding the rating(s) may cause the device breakdown, damage or deterioration, and may result injury by explosion or combustion.
- [2] Use an appropriate power supply fuse to ensure that a large current does not continuously flow in case of over current and/or IC failure. The IC will fully break down when used under conditions that exceed its absolute maximum ratings, when the wiring is routed improperly or when an abnormal pulse noise occurs from the wiring or load, causing a large current to continuously flow and the breakdown can lead smoke or ignition. To minimize the effects of the flow of a large current in case of breakdown, appropriate settings, such as fuse capacity, fusing time and insertion circuit location, are required.
- [3] If your design includes an inductive load such as a motor coil, incorporate a protection circuit into the design to prevent device malfunction or breakdown caused by the current resulting from the inrush current at power ON or the negative current resulting from the back electromotive force at power OFF. IC breakdown may cause injury, smoke or ignition.
Use a stable power supply with ICs with built-in protection functions. If the power supply is unstable, the protection function may not operate, causing IC breakdown. IC breakdown may cause injury, smoke or ignition.
- [4] Do not insert devices in the wrong orientation or incorrectly.
Make sure that the positive and negative terminals of power supplies are connected properly. Otherwise, the current or power consumption may exceed the absolute maximum rating, and exceeding the rating(s) may cause the device breakdown, damage or deterioration, and may result injury by explosion or combustion.
In addition, do not use any device that is applied the current with inserting in the wrong orientation or incorrectly even just one time.

Points to remember on handling of ICs**(1) Thermal Shutdown Circuit**

Thermal shutdown circuits do not necessarily protect ICs under all circumstances. If the thermal shutdown circuits operate against the over temperature, clear the heat generation status immediately.

Depending on the method of use and usage conditions, such as exceeding absolute maximum ratings can cause the thermal shutdown circuit to not operate properly or IC breakdown before operation.

(2) Heat Radiation Design

In using an IC with large current flow such as power amp, regulator or driver, please design the device so that heat is appropriately radiated, not to exceed the specified junction temperature (T_j) at any time and condition. These ICs generate heat even during normal use. An inadequate IC heat radiation design can lead to decrease in IC life, deterioration of IC characteristics or IC breakdown. In addition, please design the device taking into consideration the effect of IC heat radiation with peripheral components.

(3) Back-EMF

When a motor rotates in the reverse direction, stops or slows down abruptly, a current flows back to the motor's power supply due to the effect of back-EMF. If the current sink capability of the power supply is small, the device's motor power supply and output pins might be exposed to conditions beyond maximum ratings. To avoid this problem, take the effect of back-EMF into consideration in system design.

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