

TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

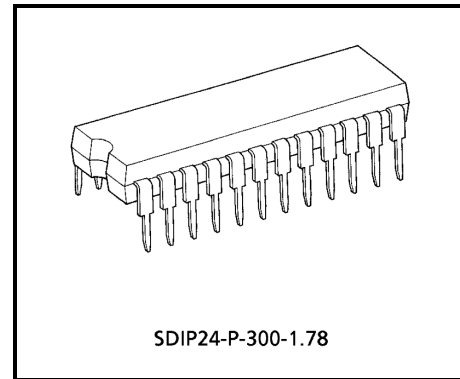
# TA8189N

## Quad Preamplifier For Double Cassette Tape Recorder

The TA8189N is a quad pre amplifier designed for use in record / play back amplifier. It is suitable for double cassette tape recorder.

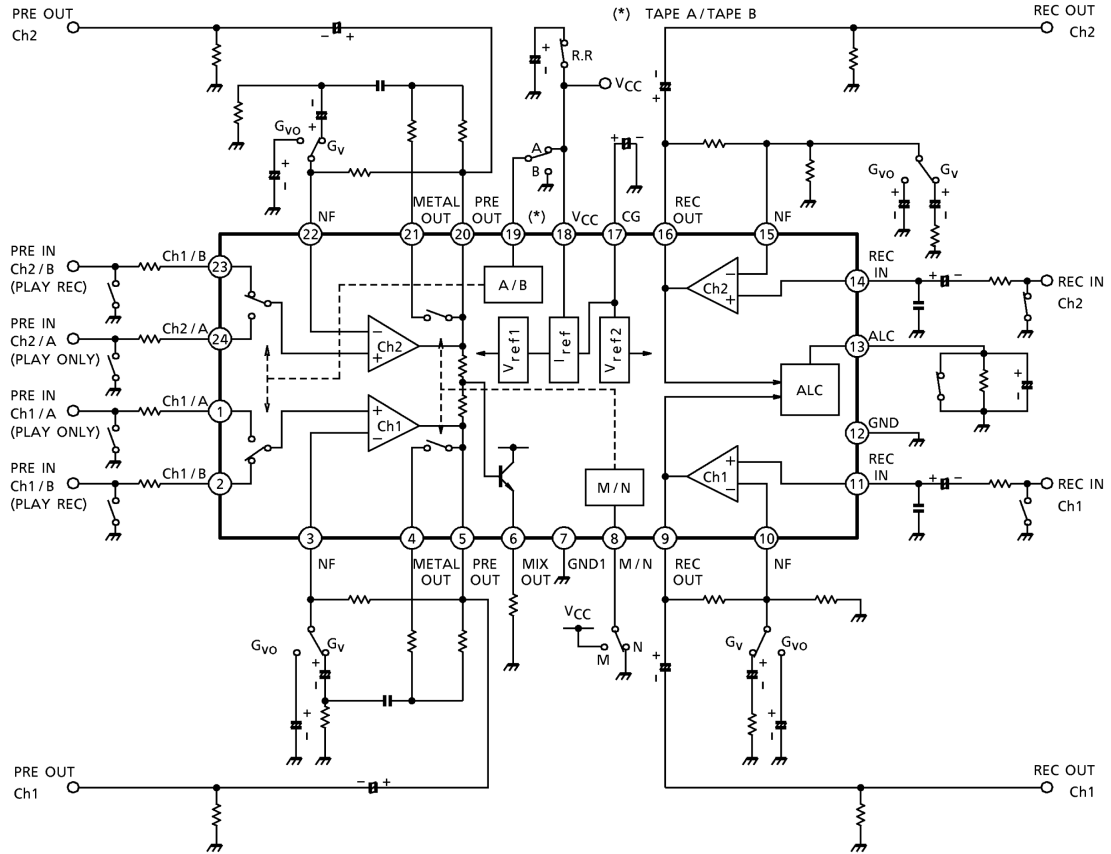
### Features

- Play back amp
  - Built in input select switch.
  - Built in equalizer control switch.
  - Mixing output, for music selection.
- Recording amp
  - Built in ALC detector circuit.
- Operating supply voltage range:  $V_{CC (opr)} = 4.0 \sim 13.5V$  ( $T_a = 25^\circ C$ )

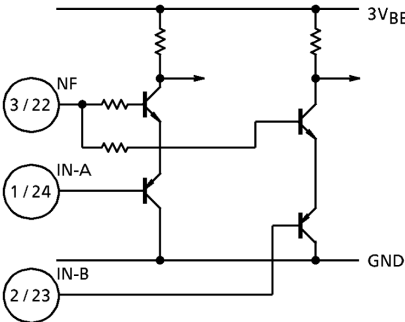
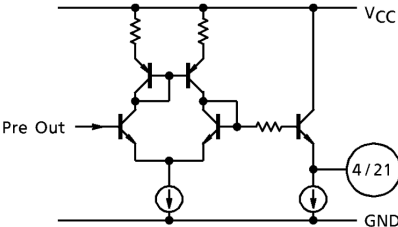
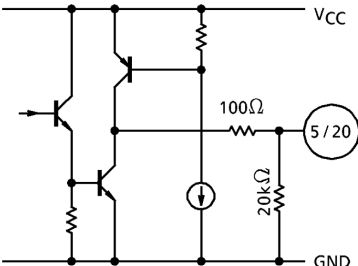
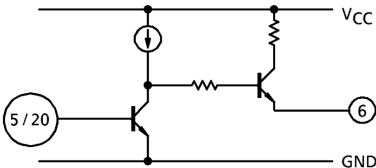
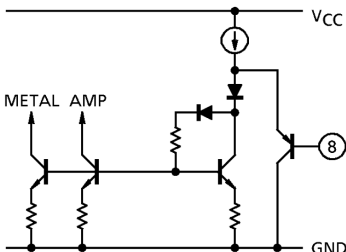


Weight: 1.2g (typ.)

## Block Diagram



## Terminal Explanation

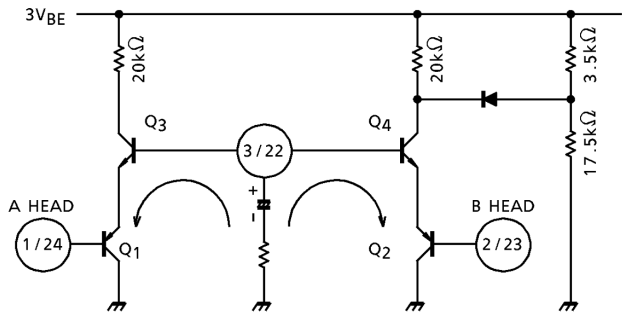
Terminal No.	Symbol	Function	Equivalent Circuit
1	Tape A in (ch1)	Tape play back input (play)	
24	Tape A in (ch1)		
2	Tape B in (ch2)	Tape play back input (play / rec)	
23	Tape B in (ch2)		
3	PB NF (ch1)	Tape play back NF	
22	PB NF (ch2)		
4 / 21	Metal out	Metal EQ switch	
5	Pre out (ch1)	Play back amp output	
20	Pre out (ch2)		
6	Mix out	Mixing output	
7	GND	GND	—
8	Metal / normal SW	Change over switch for metal mode and normal mode.	

Terminal No.	Symbol	Function	Equivalent Circuit
9	Rec out (ch1)	Recording amp output	
16	Rec out (ch2)		
10	Rec NF (ch1)	Recording amp NF	
15	Rec NF (ch2)		
11	Rec in (ch1)	Recording amp input	
14	Rec in (ch2)		
12	GND	GND	—
13	ALC T.C	Automatic level control (ALC) time constant terminal	
17	CG det.	NF charge up circuit switching terminal	

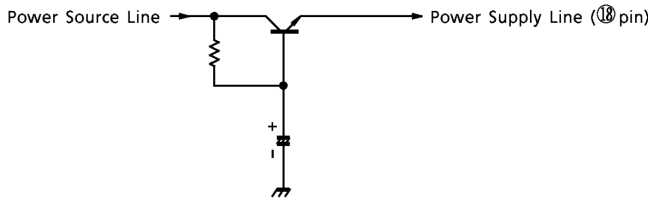
Terminal No.	Symbol	Function	Equivalent Circuit
19	Tape A / tape B SW	Play back AMP input selector	

Application Information And Application Method

1. Input level of play amp.
- In case that input voltage ( $V_{in} > 0.0245V_{rms}$  (−30dBm)) is applied to A-head and B-head at same time on a set, use A-head for reproducing only and, B-head for recording or reproducing.
- In case that the over-voltage is applied to A-head and B-head at same time, the transistor  $Q_3$ ,  $Q_4$  are made a saturation condition and NF condenser is discharged by base-current of  $Q_3$ ,  $Q_4$  and the output DC voltage of pin 3 / 22 are raised.
- In case of the high input, use B-head, because of building in the diode against saturation on  $Q_4$ .



2. Power source line
- In case of including the ripple on the power source line, stabilize by using a transistor as following figure.



**Maximum Ratings (Ta = 25°C)**

Characteristic	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	14.5	V
Power dissipation	P <sub>D</sub> (Note)	1200	mW
Operating temperature	T <sub>opr</sub>	-20~75	°C
Storage temperature	T <sub>stg</sub>	-55~150	°C

(Note) Derated above Ta = 25°C in the proportion of 9.6mW / °C.

**Electrical Characteristics (unless otherwise specified, V<sub>CC</sub> = 6V, f = 1kHz, Ta = 25°C)**

Characteristic	Symbol	Test Cir- cuit	Test Condition	Min.	Typ.	Max.	Unit
Quiescent current	I <sub>CCQ</sub>	—	Metal mode, V <sub>in</sub> = 0	—	13	20	mA
Play back amp.	Output noise voltage	V <sub>no</sub>	Normal mode, R <sub>g</sub> = 2.2kΩ, nab EQ, BW = 20Hz~20kHz, G <sub>v</sub> = 40dB	—	200	600	μV <sub>rms</sub>
	Total harmonic distortion	THD	V <sub>out</sub> = 0.2V <sub>rms</sub> , f = 1kHz normal mode	—	0.06	0.2	%
	Maximum output voltage	V <sub>om</sub>	THD = 1.0%, R <sub>L</sub> = 10kΩ, f = 1kHz, normal mode	0.5	1.0	—	V <sub>rms</sub>
	Open loop voltage gain	G <sub>vo</sub>	f = 1kHz, R <sub>L</sub> = 10kΩ, V <sub>in</sub> = 13.8μV (-95dBm)	70	95	—	dB
	Cross talk	C.T. (ch)	V <sub>out</sub> = 0.775V <sub>rms</sub> (0dBm), f = 1kHz, R <sub>g</sub> = 2.2kΩ, normal mode	-40	-60	—	dB
	Tape A / tape B cross talk	C.T. (in)	V <sub>out</sub> = 0.775V <sub>rms</sub> (0dBm), f = 1kHz, R <sub>g</sub> = 2.2kΩ, normal mode	—	-66	—	dB
	Ripple rejection ratio	R.R.	V <sub>ripple</sub> = 0.775V <sub>rms</sub> (0dBm), f <sub>ripple</sub> = 100Hz, R <sub>g</sub> = 2.2kΩ, normal mode	—	-38	—	dB
	Voltage gain	G <sub>vn</sub>	V <sub>in</sub> = 7.75mV <sub>rms</sub> (-40dBm), f = 1kHz, R <sub>L</sub> = 10kΩ, normal nab	—	40	—	dB
Pre amp →rec amp C.T.	C.T. (P / R)	—	f = 1kHz, V <sub>out</sub> (pre) = 0.775V <sub>rms</sub> (0dBm), normal (pre)	—	-53	—	dB
Rec amp →pre amp C.T.	C.T. (R / P)	—	f = 1kHz, V <sub>out</sub> (rec) = 0.775V <sub>rms</sub> (0dBm), normal (pre)	—	-76	—	dB

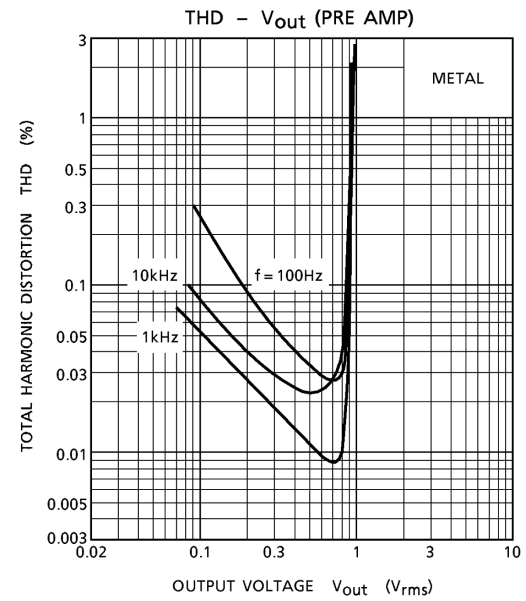
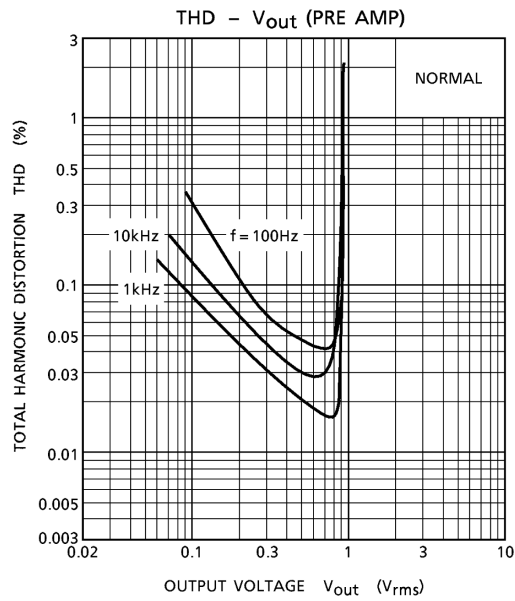
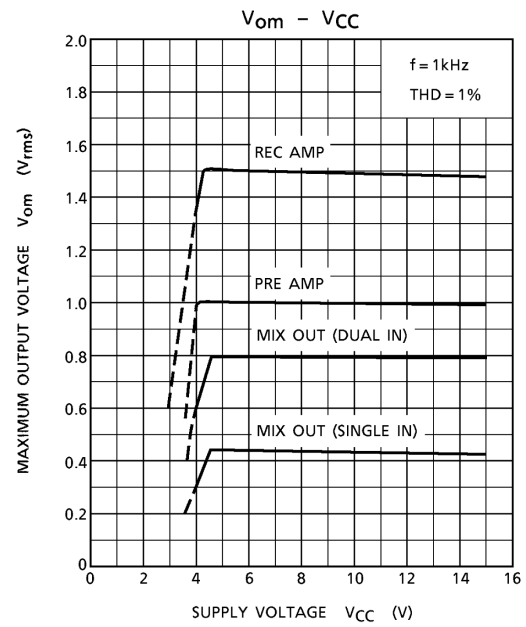
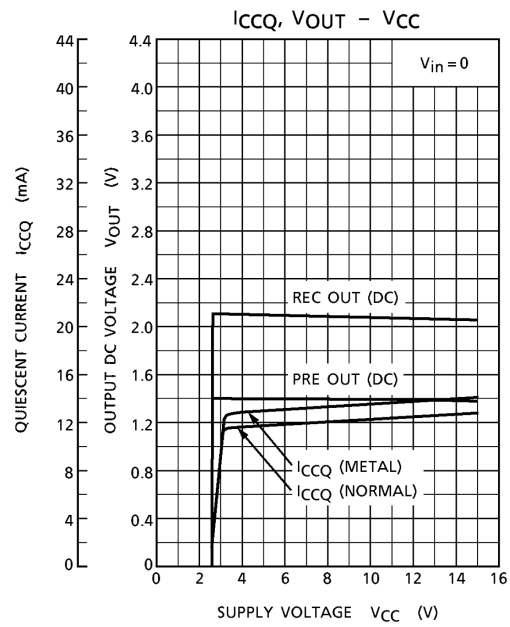
Characteristic		Symbol	Test Cir-cuit	Test Condition	Min.	Typ.	Max.	Unit
Recording amp.	Output noise voltage	V <sub>no</sub>	—	R <sub>g</sub> = 2.2kΩ, BW = 20Hz~20kHz, ALC off G <sub>V</sub> = 60dB	—	1.35	2.7	mV
	Total harmonic distortion	THD	—	V <sub>out</sub> = 0.5V <sub>rms</sub> , f = 1kHz, ALC off R <sub>L</sub> = 10kΩ	—	0.37	1.0	%
	Maximum output voltage	V <sub>om</sub>	—	THD = 1%, R <sub>L</sub> = 10kΩ, f = 1kHz, ALC off	1.2	1.5	—	V <sub>rms</sub>
	Open loop voltage gain	G <sub>vo</sub>	—	f = 1kHz, R <sub>L</sub> = 10kΩ, ALC off, V <sub>in</sub> = 3.16μV <sub>rms</sub> (−110dBV)	80	108	—	dB
	ALC range	R (ALC)	—	3dB up, f = 1kHz, dual input	—	52	—	dB
	Total harmonic distortion (ALC)	THD (ALC)	—	V <sub>in</sub> = 0.0775V <sub>rms</sub> (−20dBm), f = 1kHz dual input, R <sub>L</sub> = 10kΩ	—	0.48	1.0	%
	ALC balance	B (ALC)	—	V <sub>in</sub> = 0.0775V <sub>rms</sub> (−20dBm), dual input, R <sub>L</sub> = 10kΩ, f = 1kHz	—	0	2	dB
	ALC level	V (ALC)	—	V <sub>in</sub> = 0.0775V <sub>rms</sub> (−20dBm), f = 1kHz, R <sub>L</sub> = 10kΩ	0.75	1.0	1.2	V <sub>rms</sub>
	Ripple rejection ratio	R.R.	—	V <sub>ripple</sub> = 0.775V <sub>rms</sub> (0dBm), f = 100Hz, R <sub>g</sub> = 2.2kΩ	—	−30	—	dB
	Voltage gain	G <sub>Vn</sub>	—	f = 1kHz (flat), R <sub>L</sub> = 10kΩ, V <sub>in</sub> = 1mV <sub>rms</sub> (−60dBV)	—	61	—	dB
	Cross talk (ALC off)	C.T. (ch)	—	V <sub>out</sub> = 0.775V <sub>rms</sub> (0dBm), f = 1kHz, R <sub>g</sub> = 2.2kΩ, ALC off, V <sub>in</sub> = 1mV <sub>rms</sub> (−60dBV)	−40	−54	—	dB
	Cross talk (ALC on)	C.T. (ALC)	—	V <sub>out</sub> = 0.775V <sub>rms</sub> (0dBm), f = 1kHz, R <sub>g</sub> = 2.2kΩ, ALC on, V <sub>in</sub> = 0.0775V <sub>rms</sub> (−20dBm)	−40	−54	—	dB

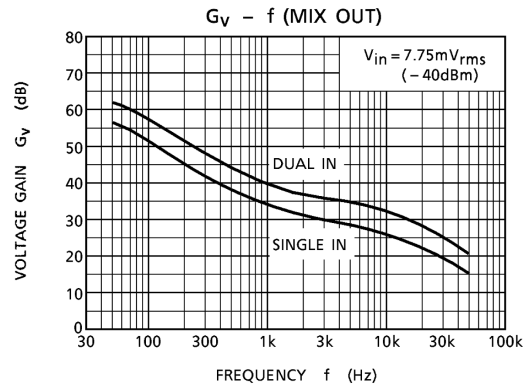
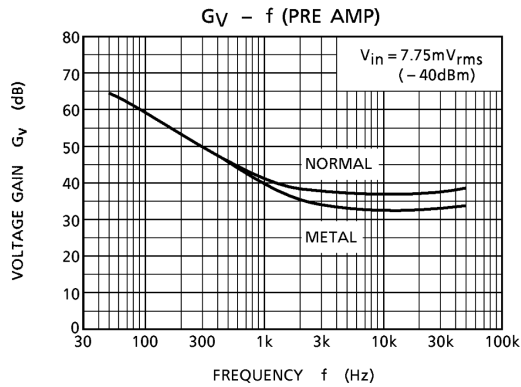
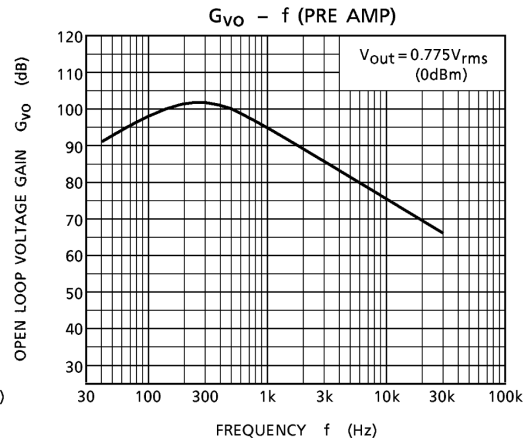
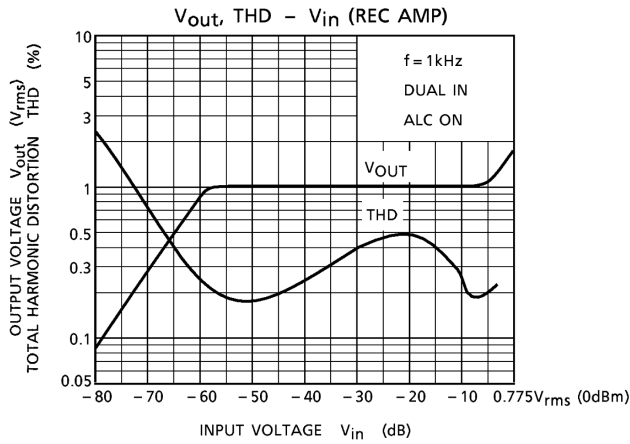
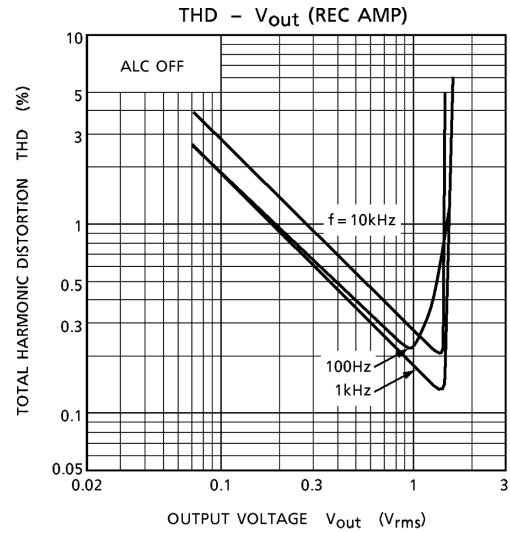
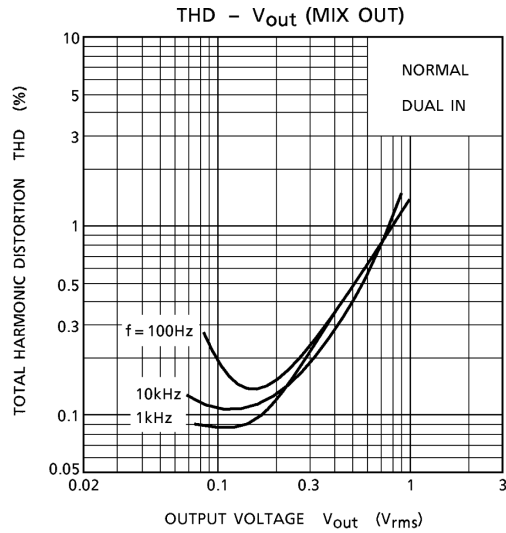
The schematic diagram illustrates the internal circuitry of the Philips R5000 cassette deck, organized around a central integrated circuit (IC) with 24 pins. The circuit is divided into several functional sections:

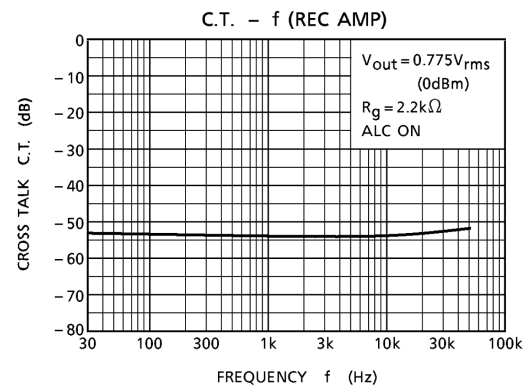
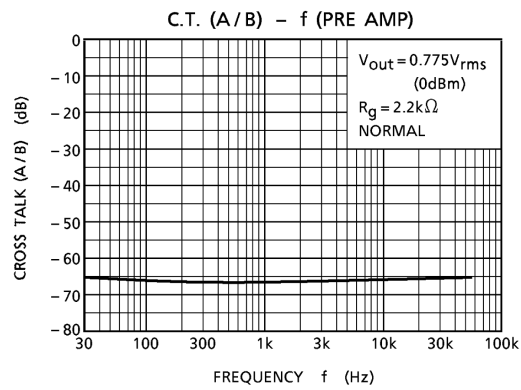
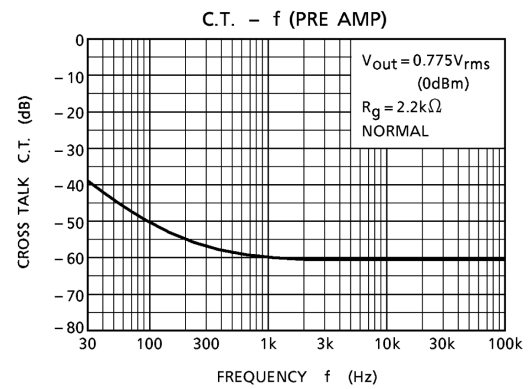
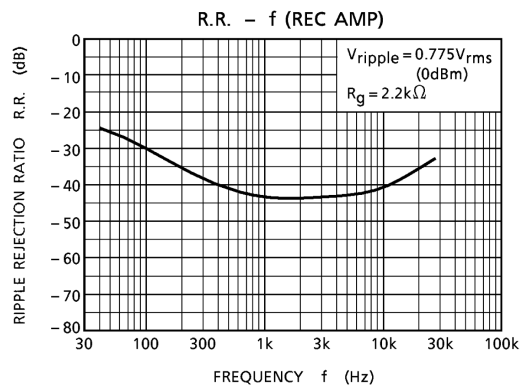
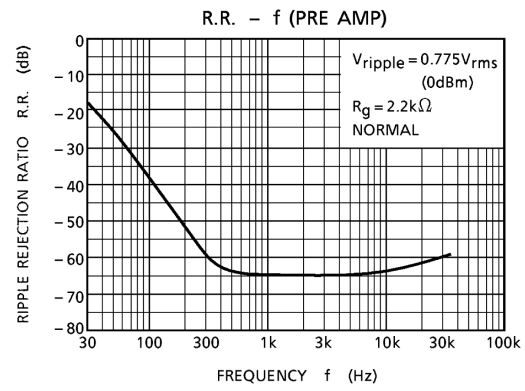
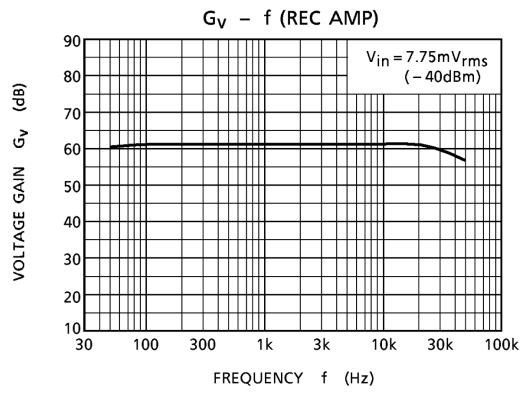
- Input Section (Pins 1-4):** Receives signals from PRE IN Ch1/B (PLAY REC), PRE IN Ch2/A (PLAY ONLY), PRE IN Ch1/A (PLAY ONLY), and PRE IN Ch1/B (PLAY REC). These signals pass through 1kΩ resistors to pins 23, 24, 1, and 2 respectively.
- Output Section (Pins 19-21):** Provides PRE OUT signals. Pin 21 is connected to a 0.018μF capacitor and a 9.1kΩ resistor. Pin 20 is connected to a 6.8kΩ resistor. Pin 19 is connected to a 10kΩ resistor and a 3.3μF capacitor.
- Control Section (Pins 13-14):** Includes an ALC (Automatic Level Control) block and a GND connection. Pin 13 is connected to a 1MΩ resistor and a 47μF capacitor. Pin 14 is connected to a 3.3μF capacitor and a 2.2kΩ resistor.
- Power and Biasing (Pins 17-18):** VCC (Pin 18) and CG (Pin 17) pins are connected to a 2.2μF capacitor and a 180kΩ resistor. Pin 16 is connected to a 3.3μF capacitor and a 10kΩ resistor.
- Signal Processing (Pins 11-12):** Includes a REC IN Ch1 (Pin 11) and a REC IN Ch2 (Pin 12) input, both connected to a 3.3μF capacitor and a 2.2kΩ resistor. Pin 10 is connected to a 300kΩ resistor and a 180kΩ resistor.
- Internal Blocks:** The IC contains several internal blocks, including A/B (Automatic Reversal), M/N (Manual/Normal), Vref1, Iref, Vref2, and Ch1/Ch2 (Channel 1/2). These blocks are interconnected with various resistors and capacitors to provide precise signal processing and control.

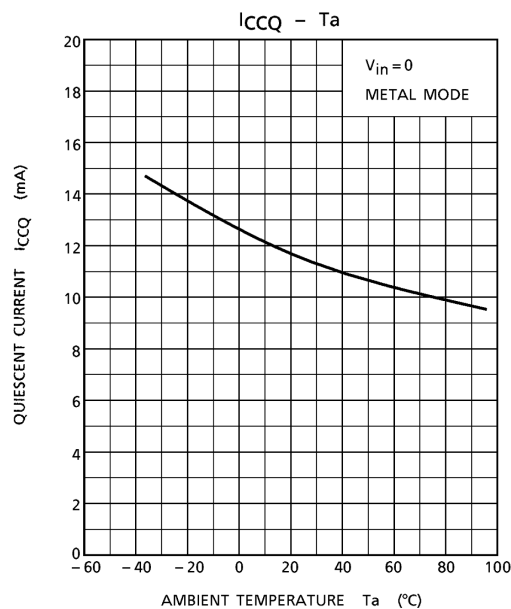
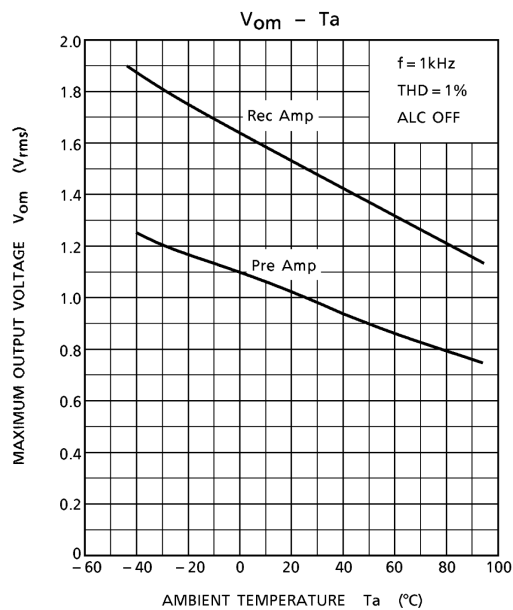
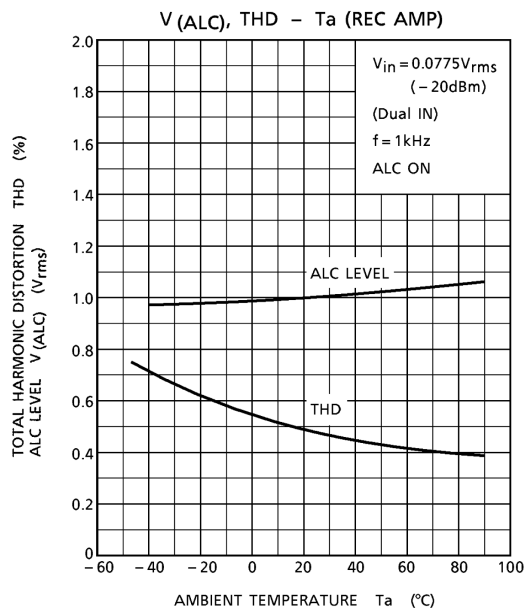
The diagram also shows the connection of external components, such as the 10kΩ resistors at the input and output, and the 3.3μF capacitors used for timing and biasing. The overall circuit is designed to provide high-quality audio reproduction and recording capabilities.



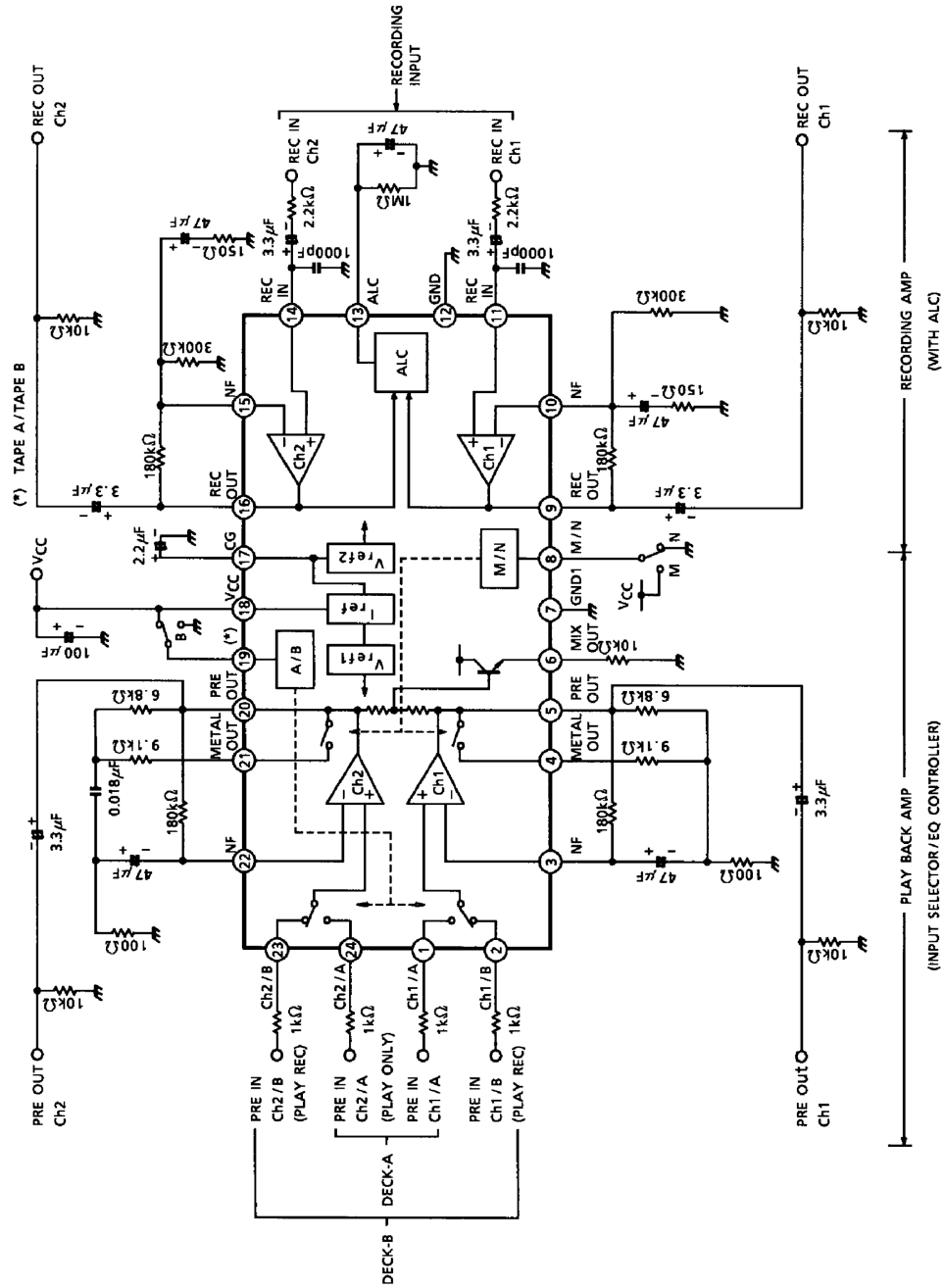








## APPLICATION CIRCUIT

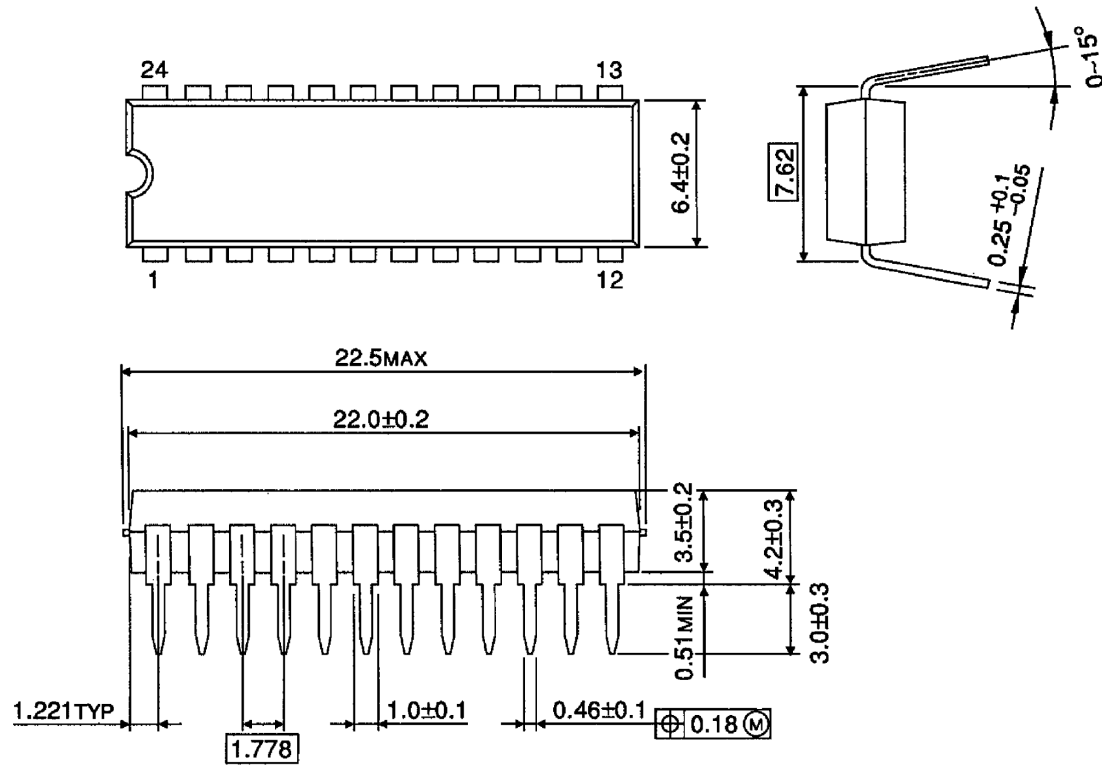


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**Package Dimensions**

SDIP24-P-300-1.78

Unit : mm



Weight: 1.2g (typ.)

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